
CA9806 4 Channel 1 ~ 15 Gb/s Bit Error Rate Tester

User Manual

(V1.00) (draft)

Nov., 2014



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Storage and Shipment

The instrument can be stored or shipped at temperatures between -30°C and +80°C. The instrument should be protected from temperature extremes that may cause condensation within it.

Safety Considerations

WARNING: The instrument is not designed for outdoor use. To prevent potential fire or shock hazard, do not expose the instrument to rain or other excessive moisture.

The following general safety precautions must be observed during all phases of operation, service, and repair of this instrument. Failure to comply with these precautions or with specific warnings elsewhere in this manual violates safety standards of design, manufacture, and intended use of the instrument. UC Instruments Corp. assumes no liability for the customer's failure to comply with these requirements.

Before operation, you should review the instrument and manual for safety markings and instructions. You must follow these to ensure safe operation and to maintain the instrument in safe condition.

WARNING: To avoid hazardous electrical shock, do not perform electrical tests when there are signs of shipping damage to any portion of the outer enclosure (covers, panels, and so on).

Line Power Connection

WARNING: To avoid the possibility of injury or death, you must observe the following precautions before switching on the instrument.

- Do not remove protective covers. Operating personnel must not remove instrument covers. Component replacement and internal adjustments must be made only by qualified service personnel.
- Instruments that appear damaged or defective should be made inoperative and secured against unintended operation until they can be repaired by qualified service personnel.
- Defective, damaged, or malfunctioning laser sources must be returned to UC Instruments Corp. Maintenance Service Center.
- Do not operate the instrument in the presence of flammable gases or fumes. Operation of any electrical instrument in such an environment constitutes a definite safety hazard.

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CA9806 4 CH 1 ~ 15 Gb/s Bit Error Rate Tester Mainframe

The UC INSTRUMENTS CA9806 is a high performance, easy to use, all-in-one, cost-effective, 4 CH 1 ~ 15 Gb/s Bit Error-Rate Tester(BERT) for current 40 G TOSA/ROSA components R&D and manufacturing environments as well as field installations. The CA9806 incorporates an internal reference clock, a pattern generator, clock recovery circuits, and a BER analyzer, in one compact module that provides both electrical and optical interfaces at data rates up to 17 Gb/s per channel.

The CA9806 is offered with an USB interface.

The PPRBS outputs optical NRZ waveform with bit rate within 1 ~ 17 Gbps, with settable data pattern of 2^7-1 , 2^9-1 , $2^{11}-1$, $2^{15}-1$, $2^{23}-1$, $2^{31}-1$, $2^{58}-1$, and fixed 8, 16, 32, 64-bit user-defined pattern. The BERT system is controlled by external computer via a USB port, with full software support, drivers and programming guide for automation.



Figure 1 CA9806 Mainframe

Features

- Bit rates from 1 ~ 17 Gb/s; 4 Channels
- PRBS 2^7-1 , 2^9-1 , $2^{11}-1$, $2^{15}-1$, $2^{23}-1$, $2^{31}-1$; $2^{58}-1$; user defined pattern , and 8, 16, 32, 64bit definable
- Pre-emphasis output signal functionality
- 4 CH PPG and 4 CH Error Detector were integrated in one compact mainframe
- Computer control via USB
- Cost effective solution for production

Applications

- Testing of optical transceiver modules (**CFP2, CFP4, QSFP28** , SFP+, XFP, X2, Xenpak, XPAK), transponders, linecards, and subsystems
- Testing of opto-electronic components and devices (TOSA, ROSA, lasers, etc...)
- Testing of Gb/s ICs, PCBs, electronic modules, subsystems, and systems
- Serial bus and high-speed backplane design
- Installation testing and troubleshooting in optical transport networks

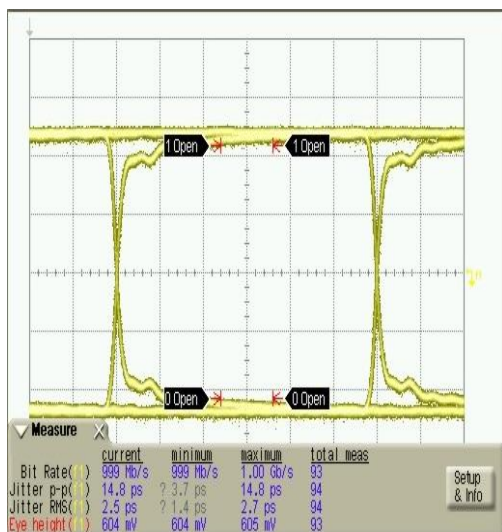
Specification

Absolute Maximum Ratings	Symbol	Min.	Typ.	Max.	Unit	Notes
Storage Temperature	Ts	-20	–	70	°C	
AC Voltage Range	VAC	90	–	246	VAC	
AC Voltage Frequency Range	VFREQ	47	–	63	Hz	
Data RF and Clock Voltage Output	VOUT	-0.5	–	1.4	V	
Data RF Voltage Input	VinData	-0.5	–	1.8	V	
Clock In Voltage Input	VinClk	0	–	1.2	V	
USB Pin Voltage	VinUSB	-0.3	–	5.5	V	
RF and Clock ESD HBM	RFesdH	-1000	–	1000	V	
RF and Clock ESD CDM	RFesdC	-250	–	250	V	
RF, Clock and USB Latchup	VI	-100	–	100	mA	
USB ESD HBM	USBesdH	-2000	–	2000	V	
USB ESD CDM	USBesdC	-500	–	500	V	
Electrical Characteristics	Symbol	Min.	Typ.	Max.	Unit	Notes
Case Temperature	Tc	5	–	45	°C	
AC Supply Current	Icc	0.75	100	–	mA	
Baud Rate (NRZ format)	BR	1	15	–	Gb/s	(Note 1)
Baud Rate Setpoint Accuracy	BRa	-10	–	+10	PPM	(Note 2)
Baud Rate PPM Offset	BRo	-999	–	999	PPM	1 PPM step size
Power On Initialization Time	Ton	–	–	15	Seconds	
Eye Phase Steps	EMp	–	–	64	Steps	2 pS per unit
Eye Amplitude Steps	EMv	–	–	128	Steps	7.8 mV per unit
Fixed Pattern Length	PL	–	–	64	Bits	
Note 1: Contact Factory for higher and lower operation						
Note 2: Aging, Temperature and Voltage						
TX Electrical	Symbol	Min.	Typ.	Max.	Unit	Notes
CML Output (Single Ended)	VoutSE	0	–	750	mVpp	AC Coupled
CML Output (Differential)	VoutDIFF	0	–	1500	mVpp	AC Coupled
CML Output (Differential) Step Size	VoutSS	–	25	–	mVpp	
CML Output (Differential) Squelch	VoutSqu	0	–	30	mVpp	
CML Output (Rise/Fall Time)	tR, tF	20	–	–	ps	20-80%
Output Impedance (differential)	Zout	–	100	–	Ω	
Termination Mismatch	TZm	–	–	5	%	At 1 MHz
AC common mode voltage	TACcm	–	–	15	mVRMS	
Differential Return Loss	SDD22	-8	–	–	dB	.01 to 10 GHz
		(Note 3)	–	–	dB	10 to 15 GHz
Common Mode Return Loss	SCD22	-6	–	–	dB	.1 to 10 GHz
		(Note 4)	–	–	dB	10 to 15 GHz

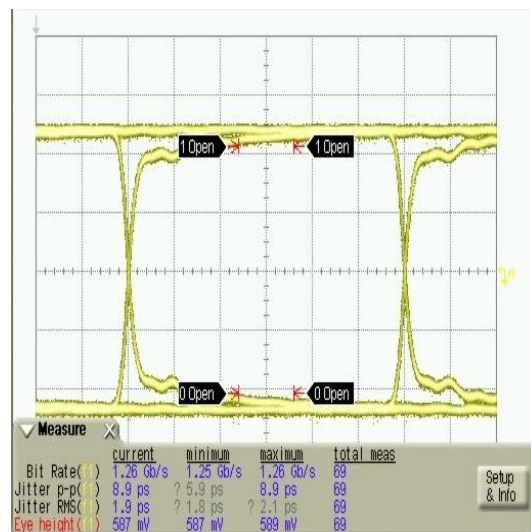
Transmitter Qsq	Tqsq	50	–	–	–	
Jitter (RMS)	TJrms	–	–	1.5	ps	(Note 5)
Jitter (PK-PK)	TJpp	–	–	8	ps	(Note 5)
Pre-Emphasis Control	TPE	–	17	–	dB	at 500 mVPPDIFF
De-Emphasis Control	TDE	–	17	–	dB	at 500 mVPPDIFF
Note 3: -8 dB + 16.6 dB/dec*log10(f/10 GHz)						
Note 4: -6 dB + 16.6 dB/dec*log10(f/10 GHz)						
Note 5: Agilent DCA-X with 50 GHz plug-in, 23-1 PRBS pattern and 500 waveforms using a precision time base trigger						
RX Electrical	Symbol	Min.	Typ.	Max.	Unit	Notes
Baud Rate Tolerance	BRt	-100	–	+100	PPM	
CML Input Voltage (Single Ended)	VinSE	100	–	800	mVpp	AC Coupled
CML Input Voltage (Differential)	VinDIFF	100	–	1600	mVpp	AC Coupled
Input Impedance (Differential)	Zin	–	100	–	Ω	
Termination Mismatch	RZm	–	–	5	%	At 1 MHz
AC common mode voltage	RACcm	–	–	25	mVRMS	
Differential Return Loss	SDD11	-12	–	–	dB	.01 to 2 GHz
		-8	–	–	dB	2 to 10 GHz
		(Note 3)	–	–	dB	10 to 15 GHz
Common Mode Return Loss	SCD11	-6	–	–	dB	.1 to 10 GHz
		(Note 4)	–	–	dB	10 to 15 GHz
CDR Acquisition Lock Time		–	–	300	mS	

Clock - Input	Symbol	Min.	Typ.	Max.	Unit	Notes
Frequency	CFin	156,248,438	156,250,000	156,251,562	Hz	Square wave
Single Ended Voltage Swing	CVpp	0.4	–	1.2	V	
Input Impedance	CRin	49.5	50	50.5	Ohm	AC coupled
Rise/Fall Time	CitR, CitF	–	–	1	nS	20%-80%
Duty Cycle	CDC	40	–	60	%	<1nS Tr/Tf
Random Jitter (RMS)	CRj	–	–	1	ps	12 kHz–20 MHz
Clock - Output	Symbol	Min.	Typ.	Max.	Unit	Notes
Programmable Divider of Line Rate	CPDLR	2	–	64	/N	Factors of 2
Single Ended Voltage Swing	CVoutSE	0	–	800	mVp	AC coupled
Squelch Voltage Output	CVsquelch	–	–	30	mVp	
Termination Mismatch	CZm	0	–	5	%	At 1 MHz
Rise/Fall Time	CotR, CotF	20	–	–	ps	20-80%
Output Return Loss	CS22	-8	–	–	dB	
Jitter (RMS)	Clrms	–	–	750	fs	(Note 5)
Jitter (PK-PK)	Clpp	–	–	3.5	ps	(Note 5)
Note 5: Using Agilent DCA-X with 50 GHz plug-in. 500 waveforms using a precision time base trigger						
Note 6: Terminate clock output if not used						

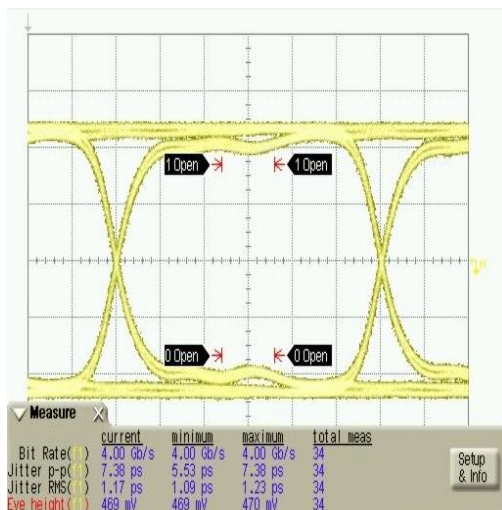
Typical Electronics Eye Diagram



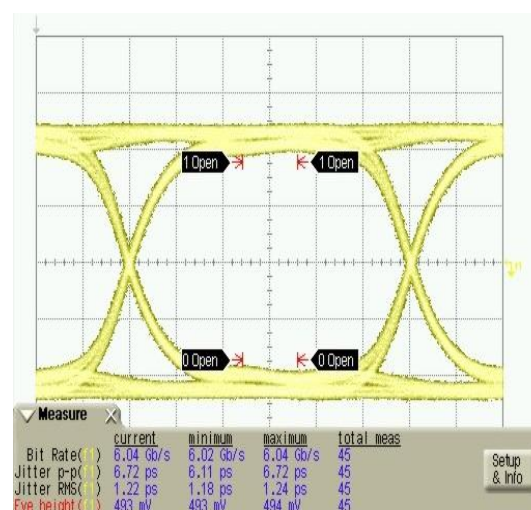
1.0 Gb/s



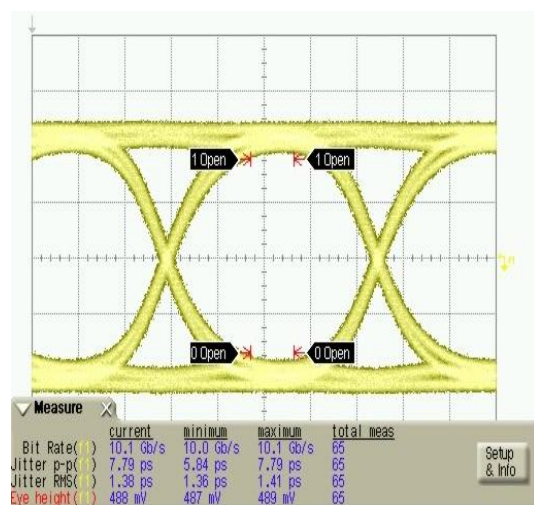
1.25 Gb/s



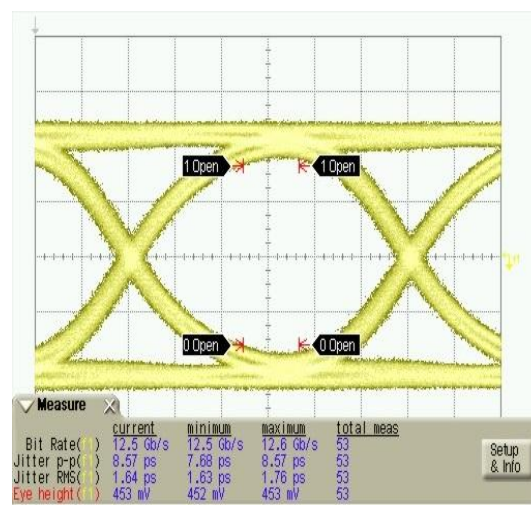
4.0Gb/s



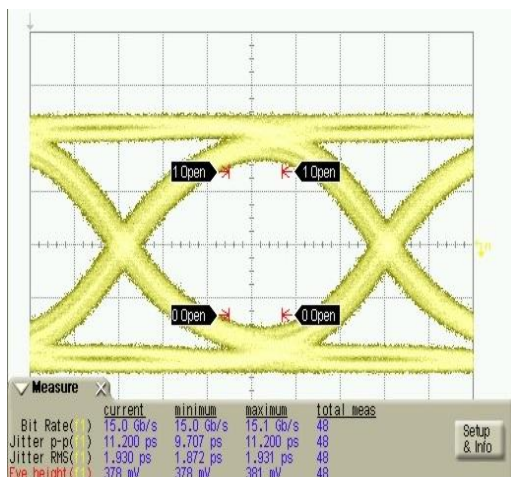
6.0 Gb/s



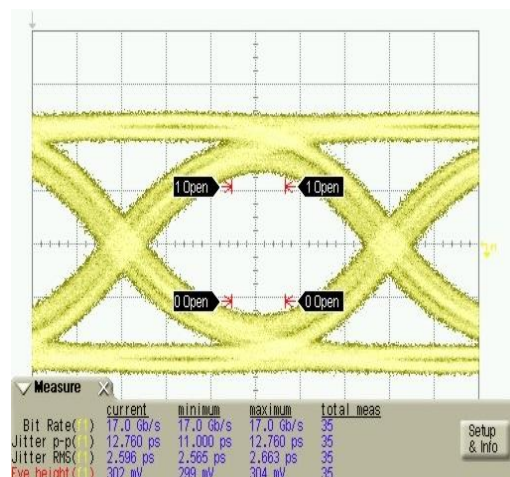
10.0Gb/s



12.5 Gb/s

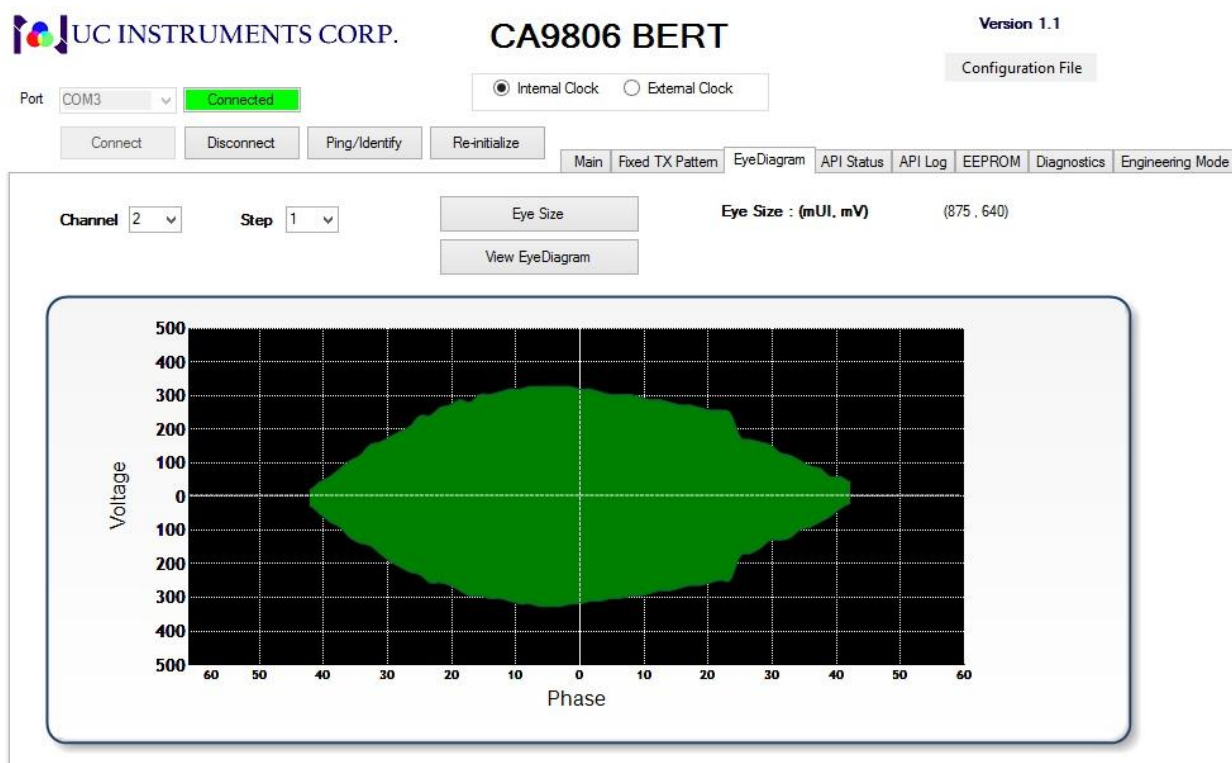


15.0 Gb/s

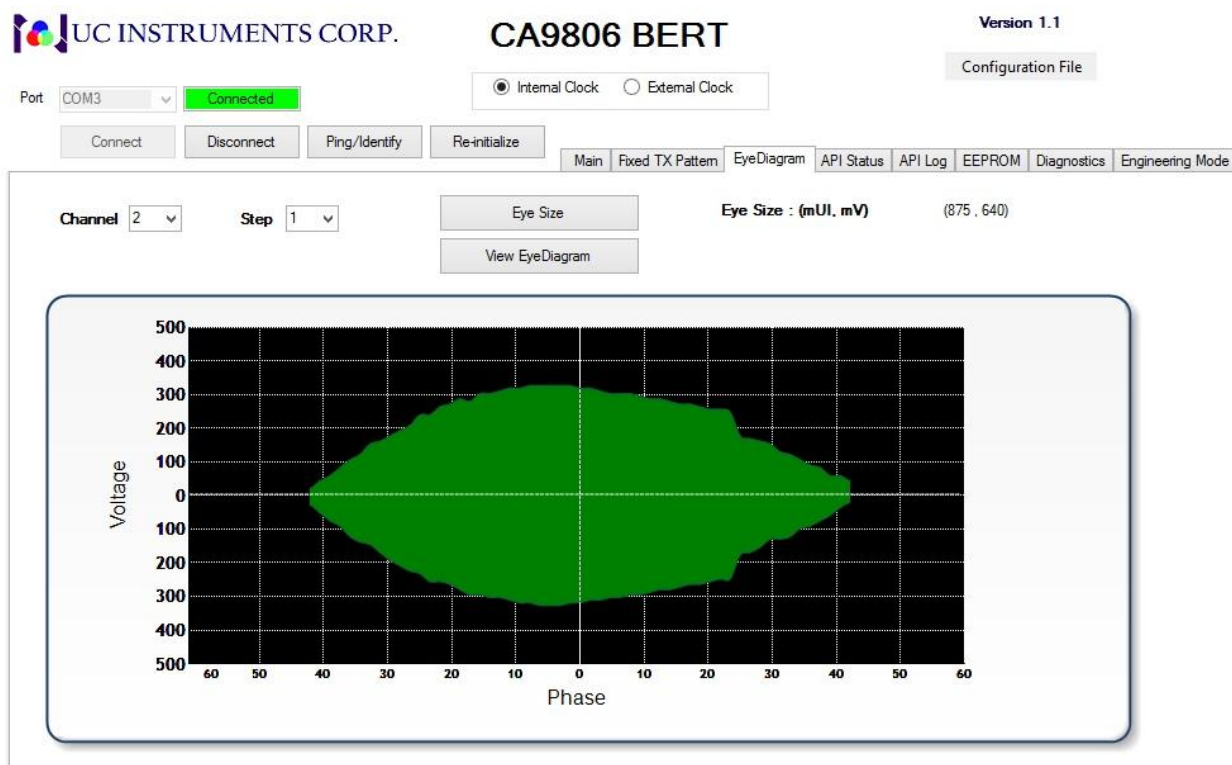


17.0 Gb/s

Buld-in Eye Diagram Testing Function



8.5 Gb/s Eye Diagram



10.0 Gb/s Eye Diagram



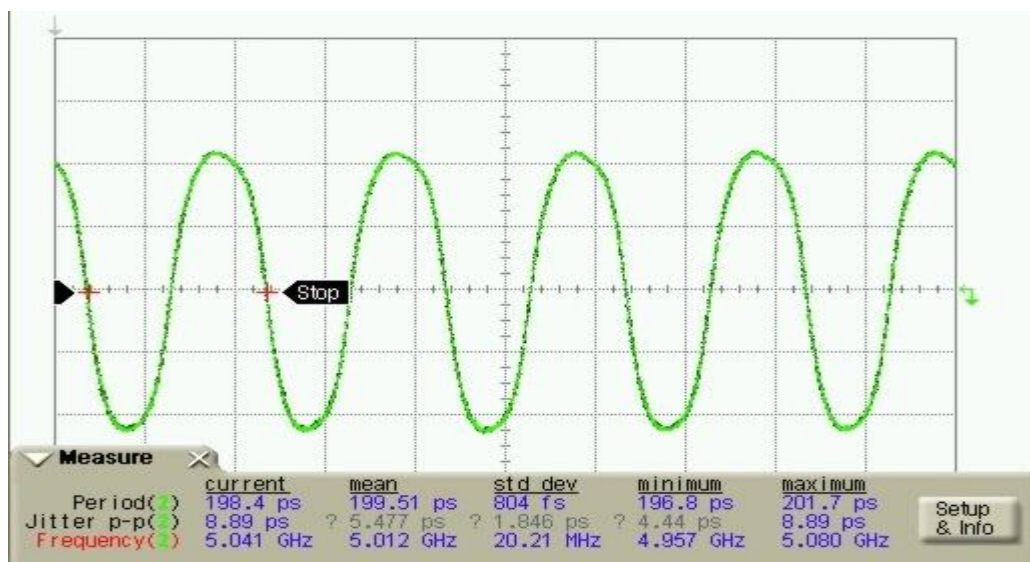
15.0 Gb/s Eye Diagram

TX Fix Pattern Output

Select Bit Size
☒ 8-bit ☐ 16-bit ☐ 32-bit ☐ 64-bit

TX Channel 1

Send Data
Clear

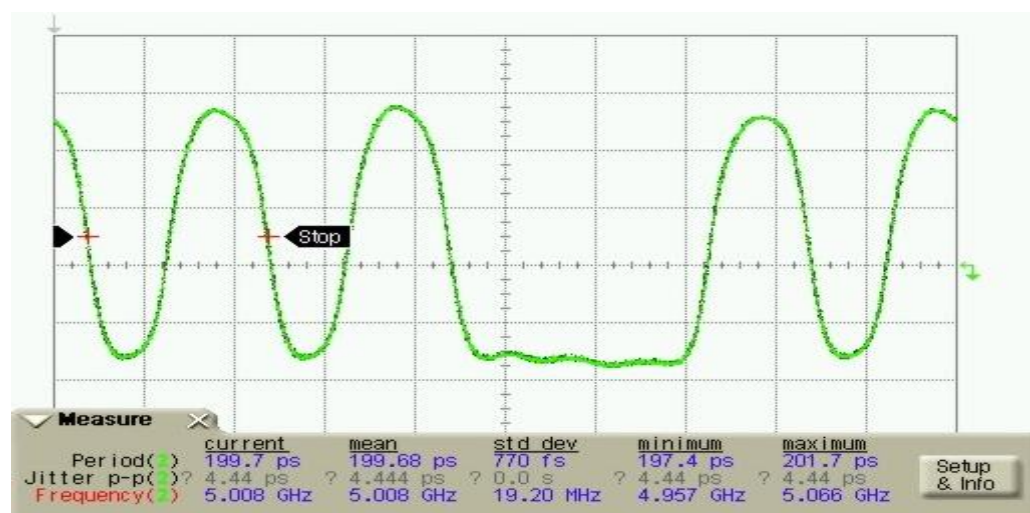


10 Gb/s Fixed TX Pattern 8-Bit 01010101 Pattern output

Select Bit Size
☒ 8-bit ☐ 16-bit ☐ 32-bit ☐ 64-bit

TX Channel 1

Send Data
Clear



10 Gb/s Fixed TX Pattern 8-Bit 01010111 Pattern output

Jitter Phase Measurement

TX Phase Jitter Measurements at 10 Gb/s using PRBS 2¹⁵ - Part 1 of 2:

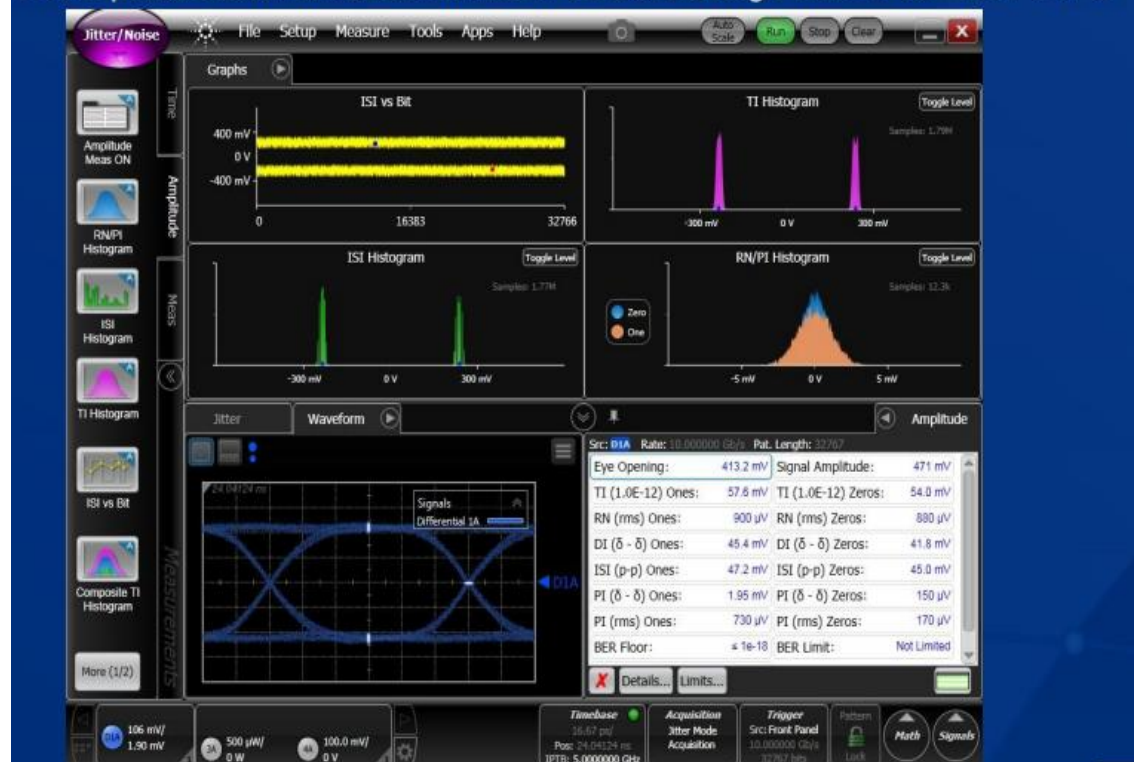


1504 TX Phase Jitter Measurements at 10 Gb/s using PRBS 2¹⁵ - Part 2 of 2:



Amplitude Jitter Measurement;

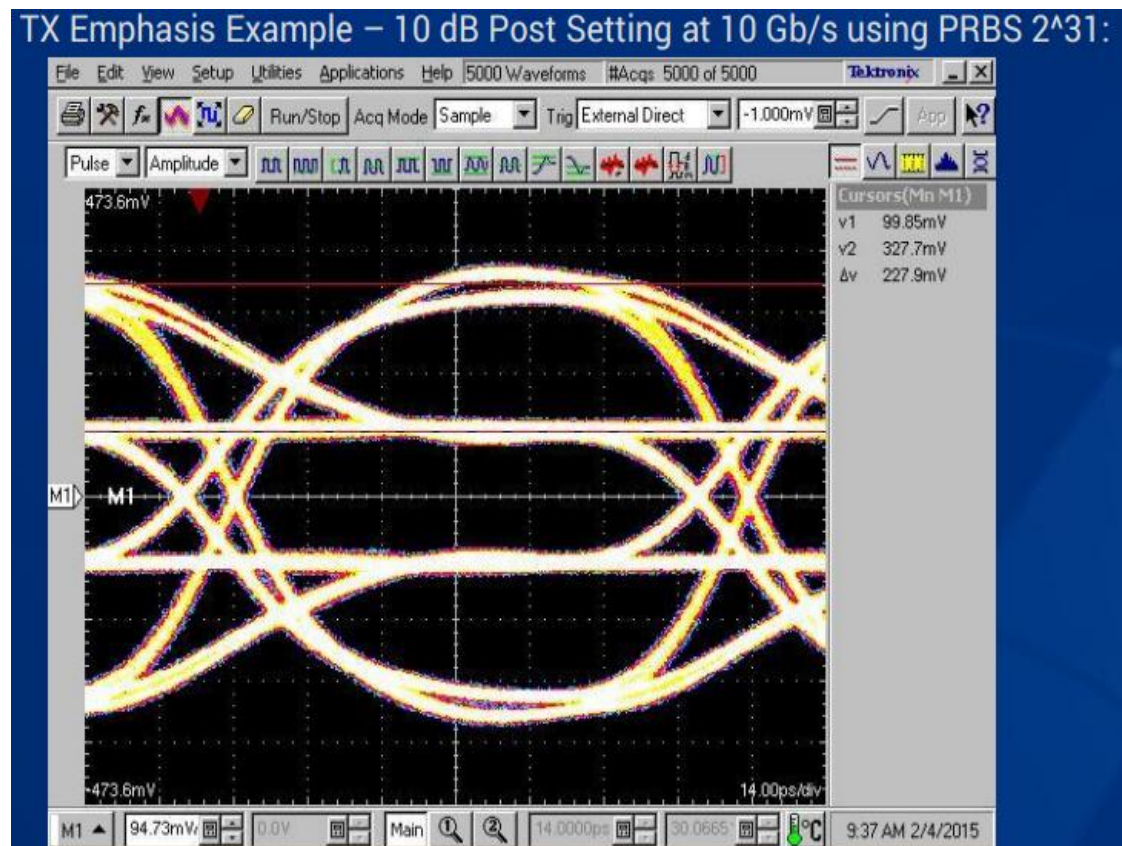
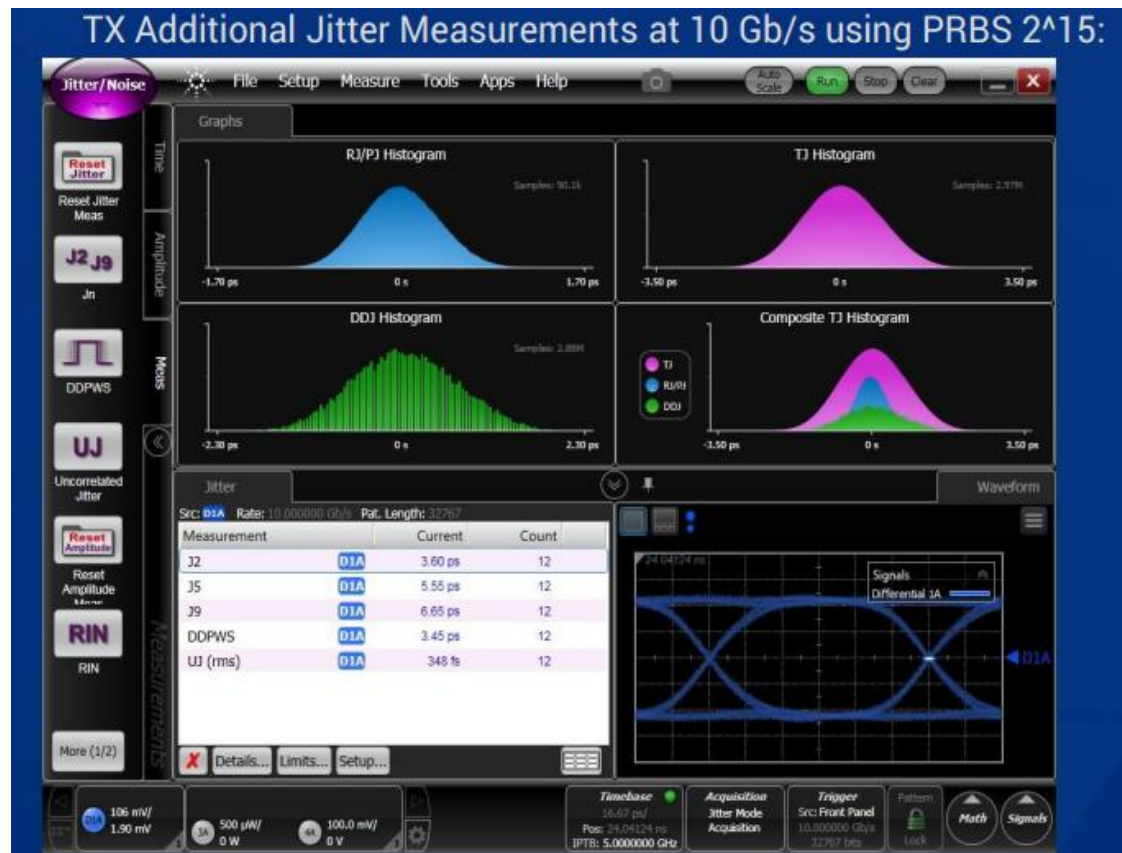
TX Amplitude Jitter Measurements at 10 Gb/s using PRBS 2¹⁵ - Part 1 of 2:



TX Amplitude Jitter Measurements at 10 Gb/s using PRBS 2¹⁵ - Part 2 of 2:



Additional Jitter and Emphasis Example



Hardware connection:

1. Link CA9806 with computer by USB cable.



2. Connect AC-DC power adaptor with CA9806 module and wall power.



3. If you have Agilent 86100C and 83483A ready to test CA9806 SMA cable connection as below:



Software Interface

CA9806 BERT

UC INSTRUMENTS CORP. CA9806 BERT Version 1.1

Port: COM3 Connected ☒ Internal Clock ☐ External Clock Configuration File

Connect Disconnect Ping/Identify Re-initialize Main Fixed TX Pattern EyeDiagram API Status API Log EEPROM Diagnostics Engineering Mode

Clock Baud Rate Kb/s	User Defined Clock Baud Rate Kb/s	PPM Offset (-999 to 999)	Trigger Frequency	Trigger Amplitude
10,312,500	<input type="text"/> Set	<input type="text"/> Set Offset	Divide by 64	500 mV

	Pattern	Amplitude	Pre-Cursor (0-31)	Post-Cursor (0-63)	Total Current (<= 32 mA)	Pre-Cursor PreEmphasis (dB)	Post-Cursor PreEmphasis (dB)	Squelch	CDR Lock	Polarity
TX Channel 1	2 ⁿ 31	25 mV	0	0	0.5	0	0	CH1 <input type="checkbox"/>	<input checked="" type="checkbox"/>	Positive Flip Polarity
TX Channel 2	2 ⁿ 31	25 mV	0	0	0.5	0	0	CH2 <input type="checkbox"/>	<input checked="" type="checkbox"/>	Positive Flip Polarity
TX Channel 3	2 ⁿ 31	25 mV	0	0	0.5	0	0	CH3 <input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Positive Flip Polarity
TX Channel 4	2 ⁿ 31	25 mV	0	0	0.5	0	0	CH4 <input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Positive Flip Polarity

	PRBS	Start BER	Stop BER	Insert Single Error	Clear BER	Bit Error Count	Time (d:hh:mm:ss)	Bit Error Rate	CDR Lock	Polarity
RX Channel 1	2 ⁿ 31	START	STOP	TX CH1	CLEAR		0		<input type="checkbox"/>	Positive Flip Polarity
RX Channel 2	2 ⁿ 31	START	STOP	TX CH2	CLEAR		0		<input type="checkbox"/>	Positive Flip Polarity
RX Channel 3	2 ⁿ 31	START	STOP	TX CH3	CLEAR		0		<input type="checkbox"/>	Positive Flip Polarity
RX Channel 4	2 ⁿ 31	START	STOP	TX CH4	CLEAR		0		<input type="checkbox"/>	Positive Flip Polarity

	Pattern	Amplitude	Pre-Cursor	Post-Cursor	Total Current (<= 32 mA)	Squelch
Clear	2 ⁿ 31	25 mV	0	0	0.5	<input checked="" type="checkbox"/> Set All TX

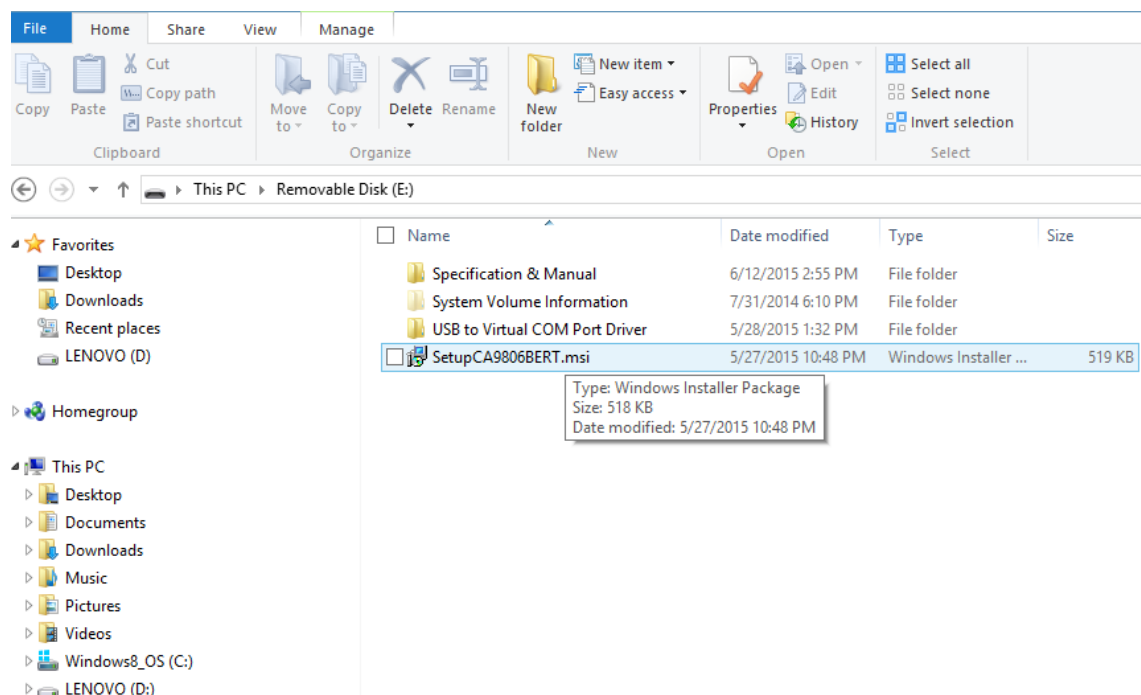
	PRBS	Start All BER	Stop All BER	Insert Single Error to All	Clear All BER	Set All RX
Clear	2 ⁿ 31	Start All BER	Stop All BER	Insert Single Error to All	Clear All BER	Set All RX

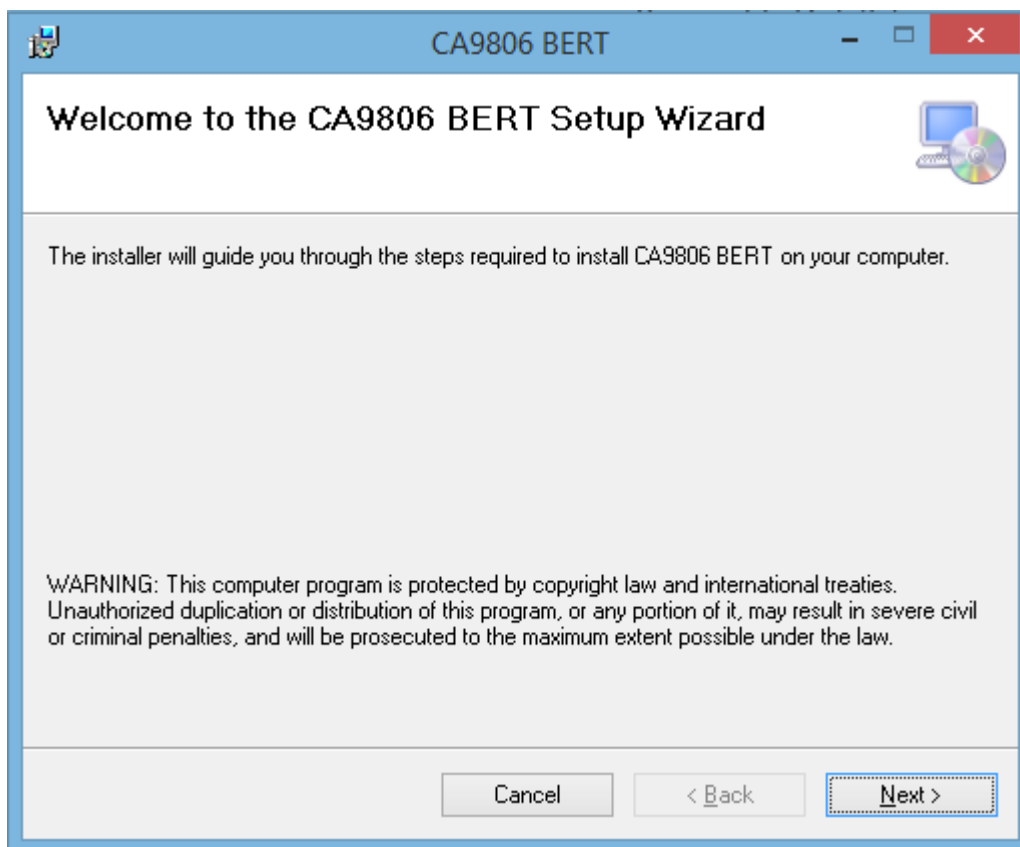
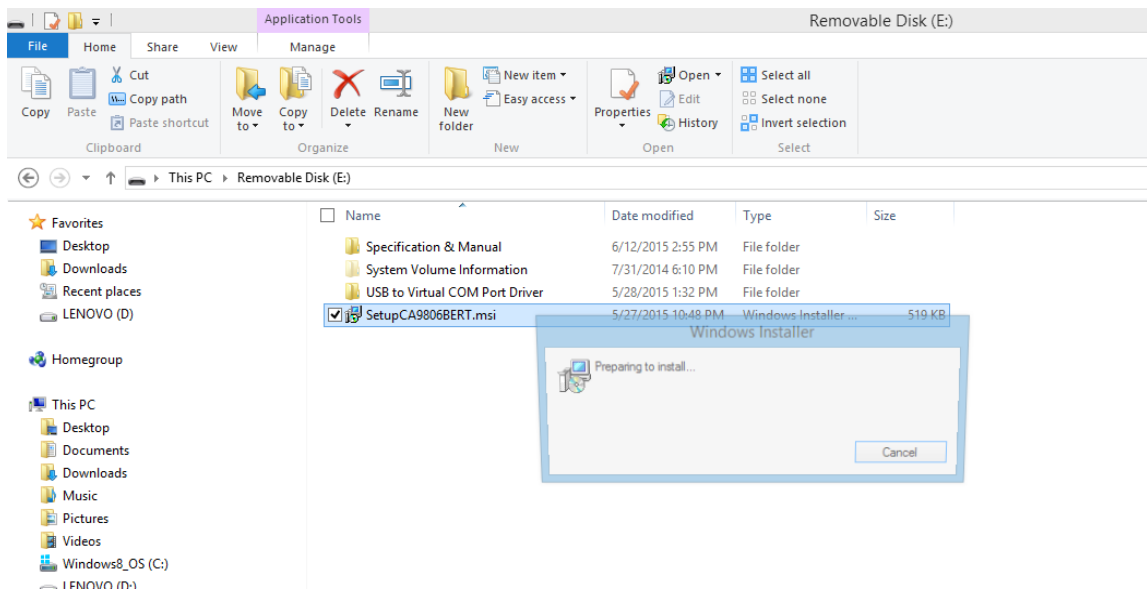
Accessories Included:

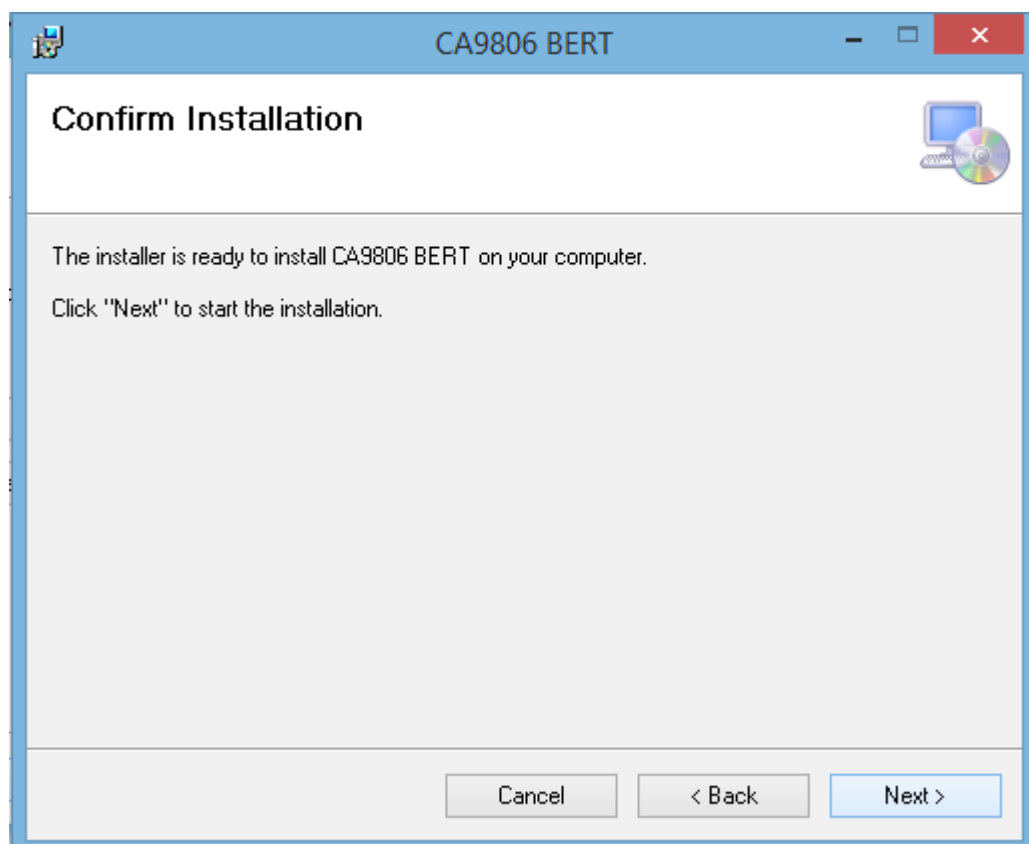
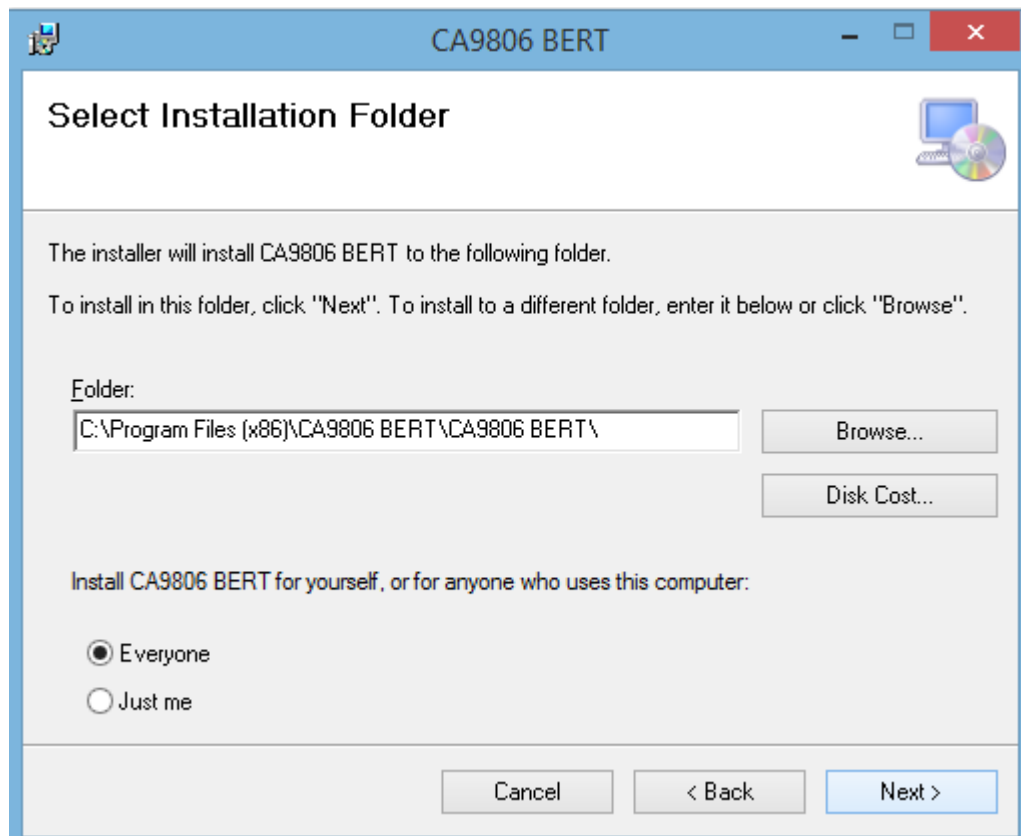
- User Software GUI
- User Manual
- AC-DC Power Adapter
- PC Interface USB cable

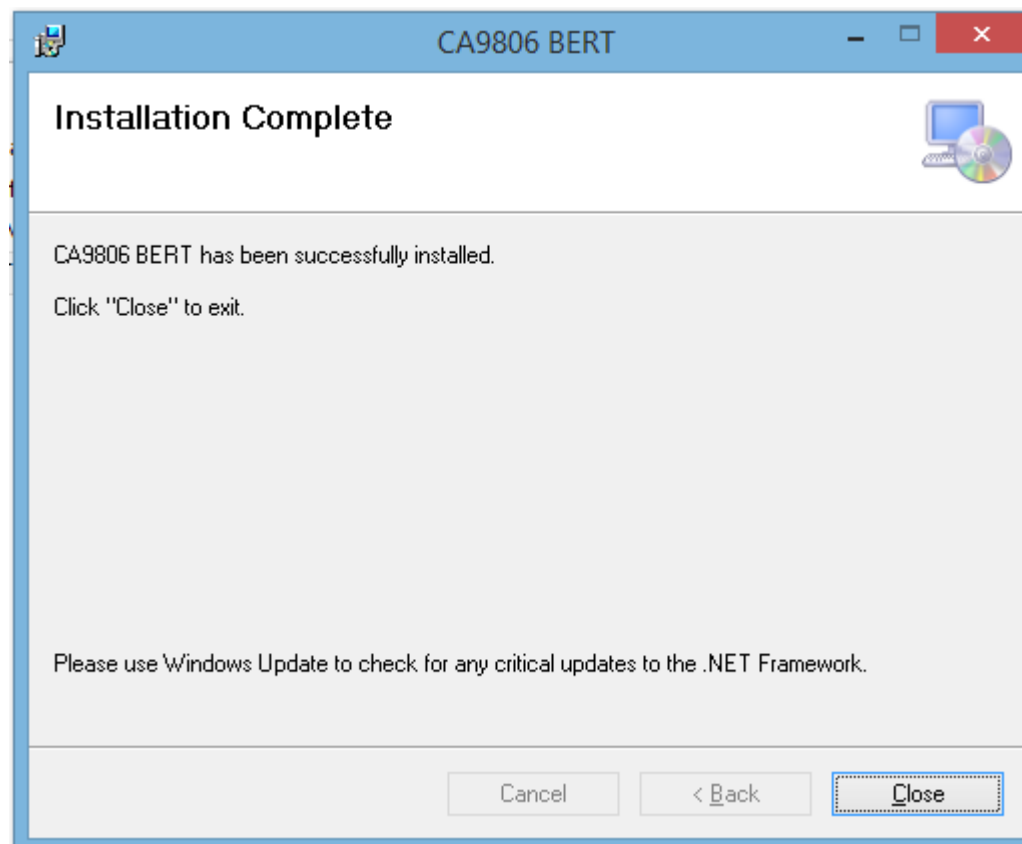
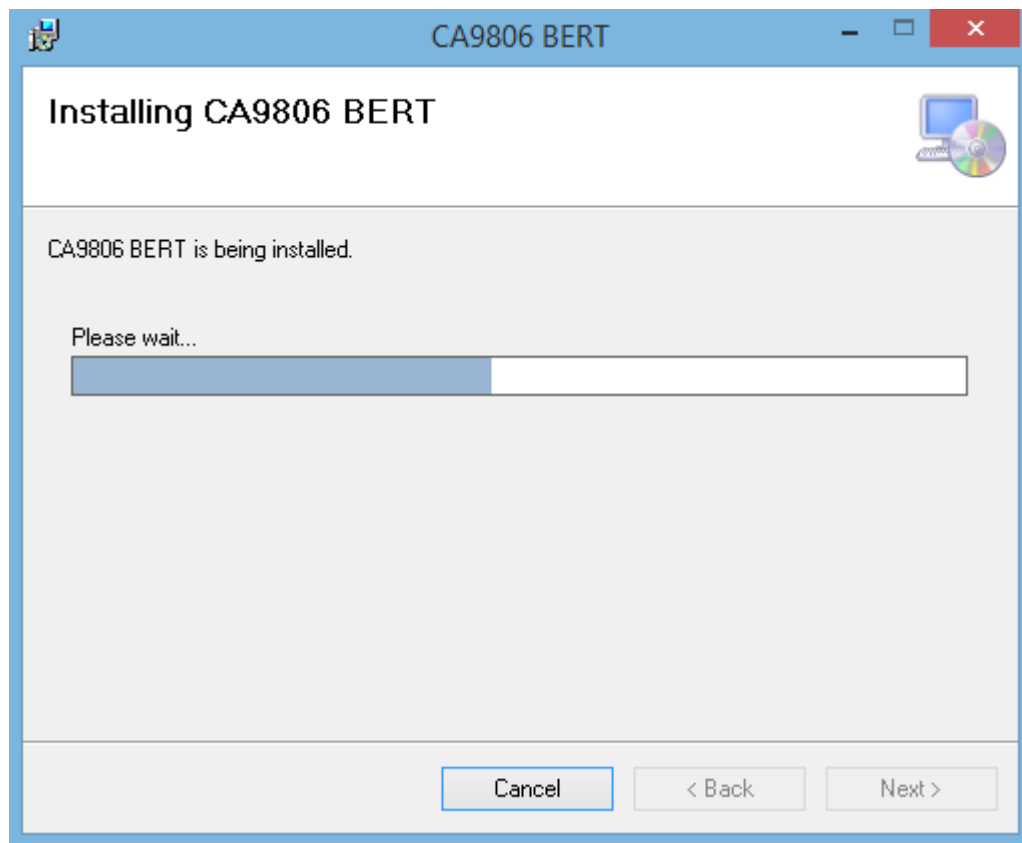
Installing the Software and Starting the Application

1. UC INSTRUEMNTS CA9806 provide an USB memory stick with CA9806 specification, user manual and user GUI software.
2. Plug the USB memory stick into one of computer USB port.
3. Find and double click "SetupCA9806BERT.msi" in the USB driver files, the application setup will be installed.

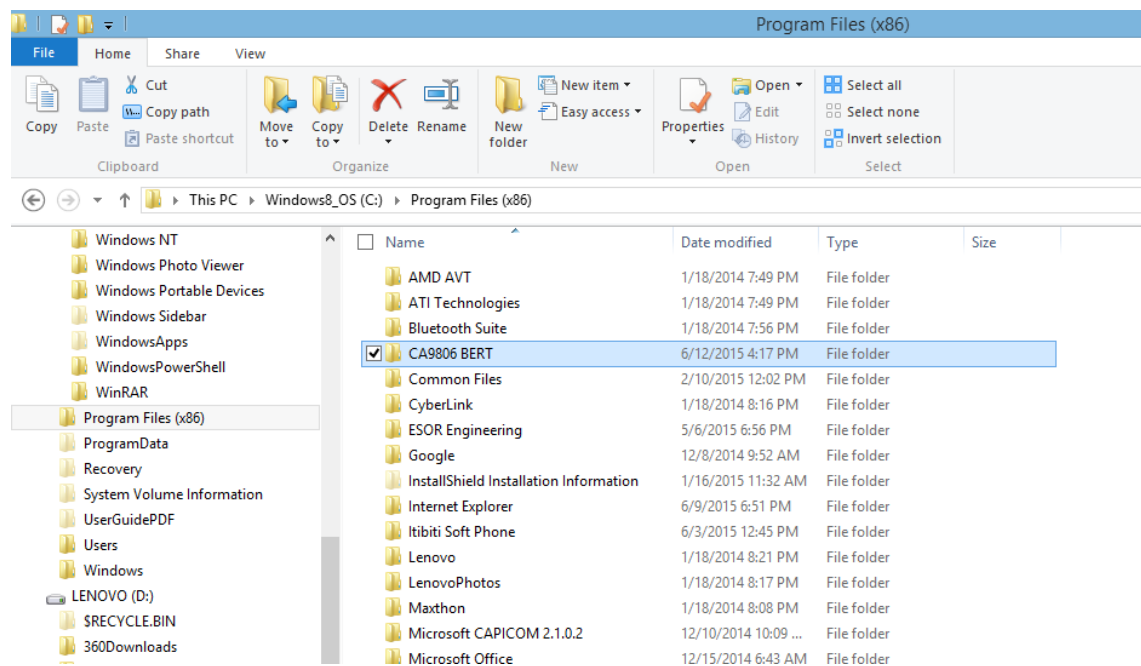
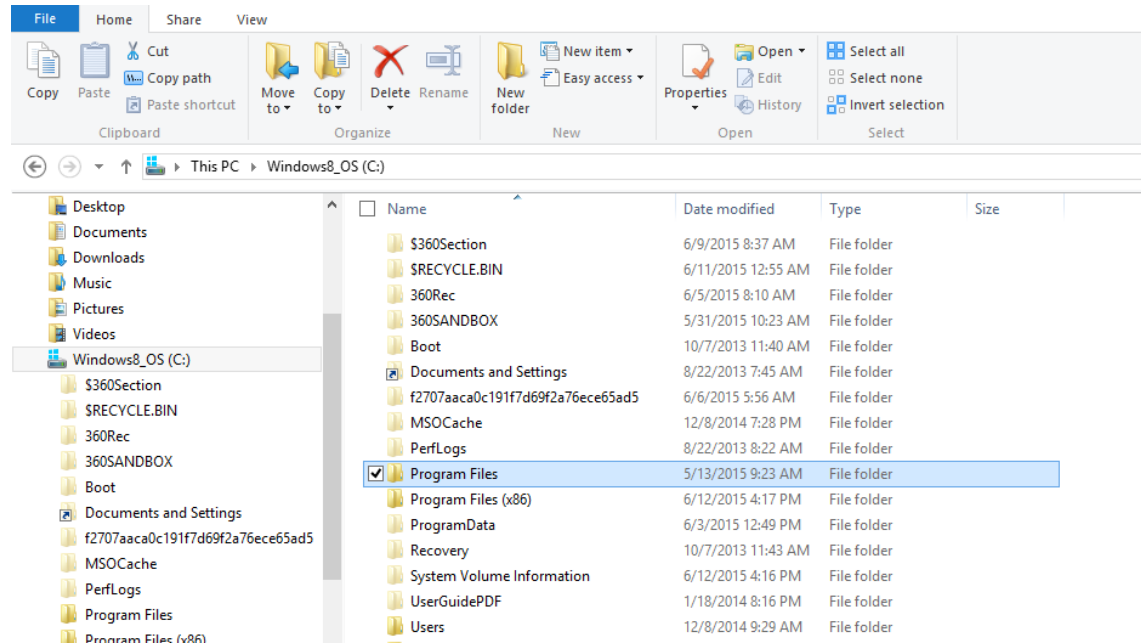


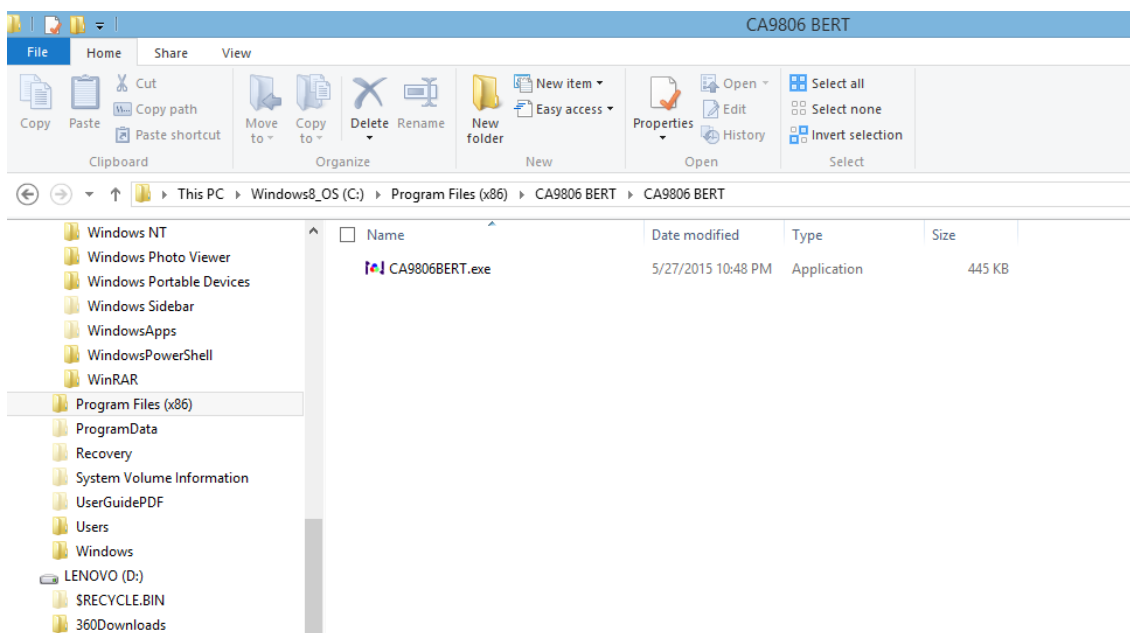
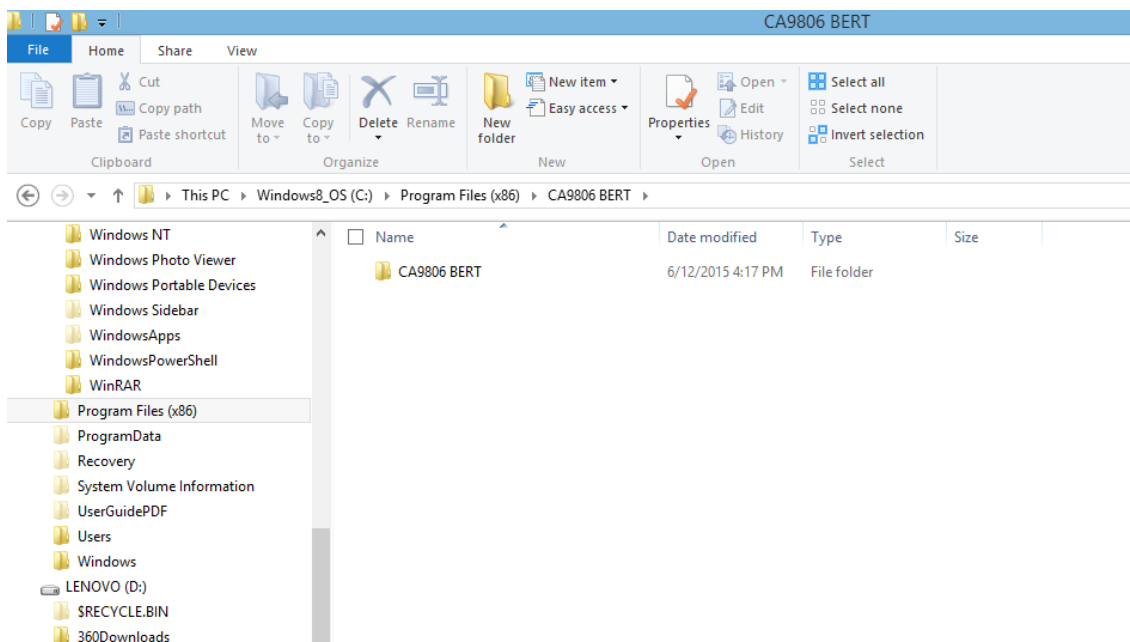




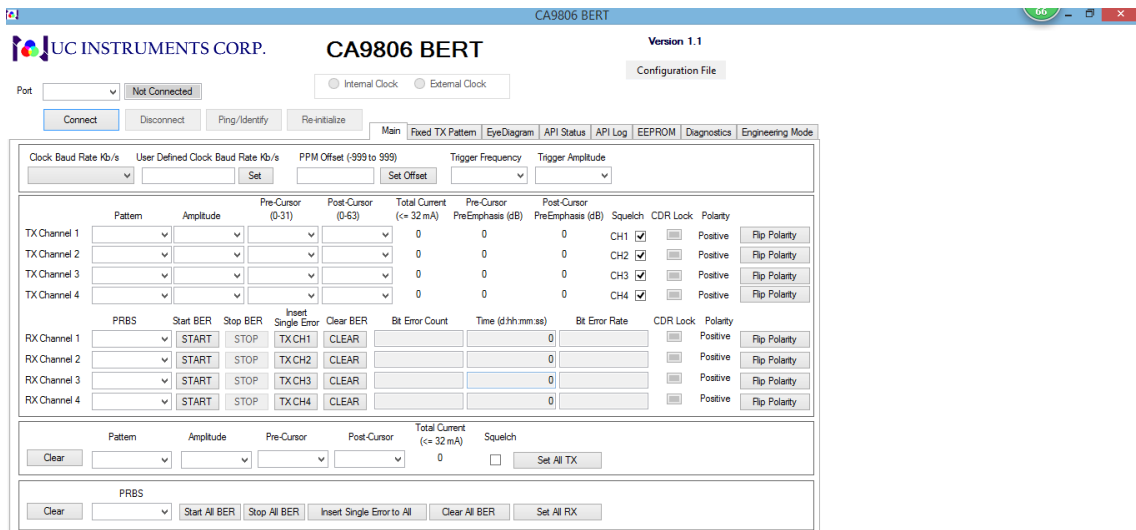


Go to:





Double click “CA9806BERT.exe” CA9806 GUI was installed and run as below:



4. If computer does not find the driver, go to "Control Panel"-->"Hardware and Sound"

-->"Devices and Printers"-->"Device Manger" to update USB driver.

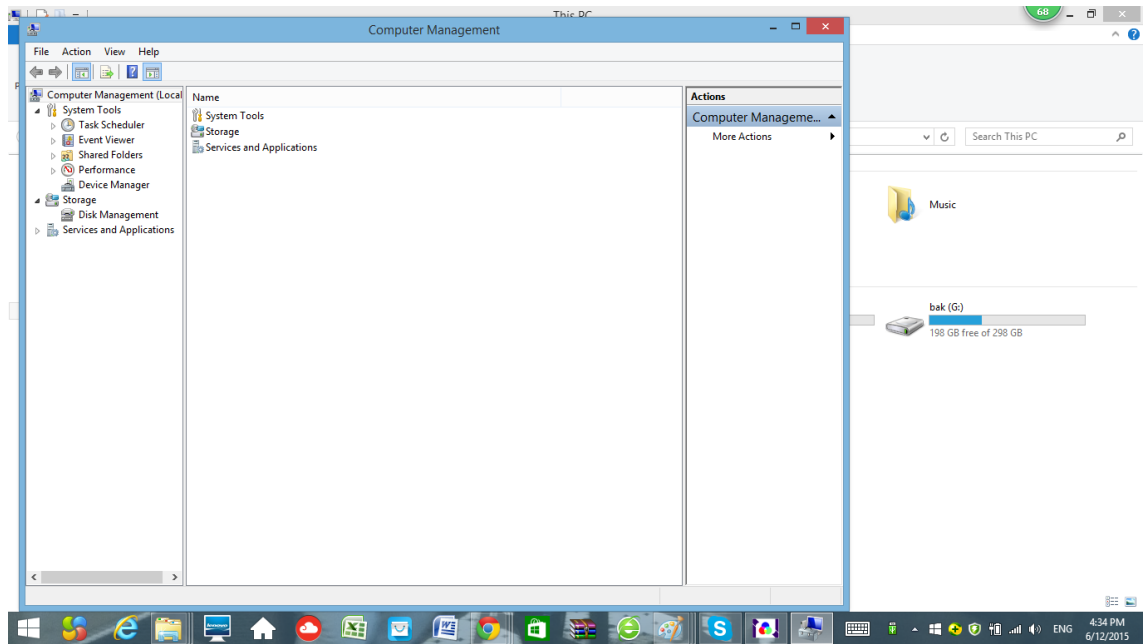
Got to USB stick “ Driver” fold → “USB to Virtual COM Port Driver” → install Virtual COM port driver.

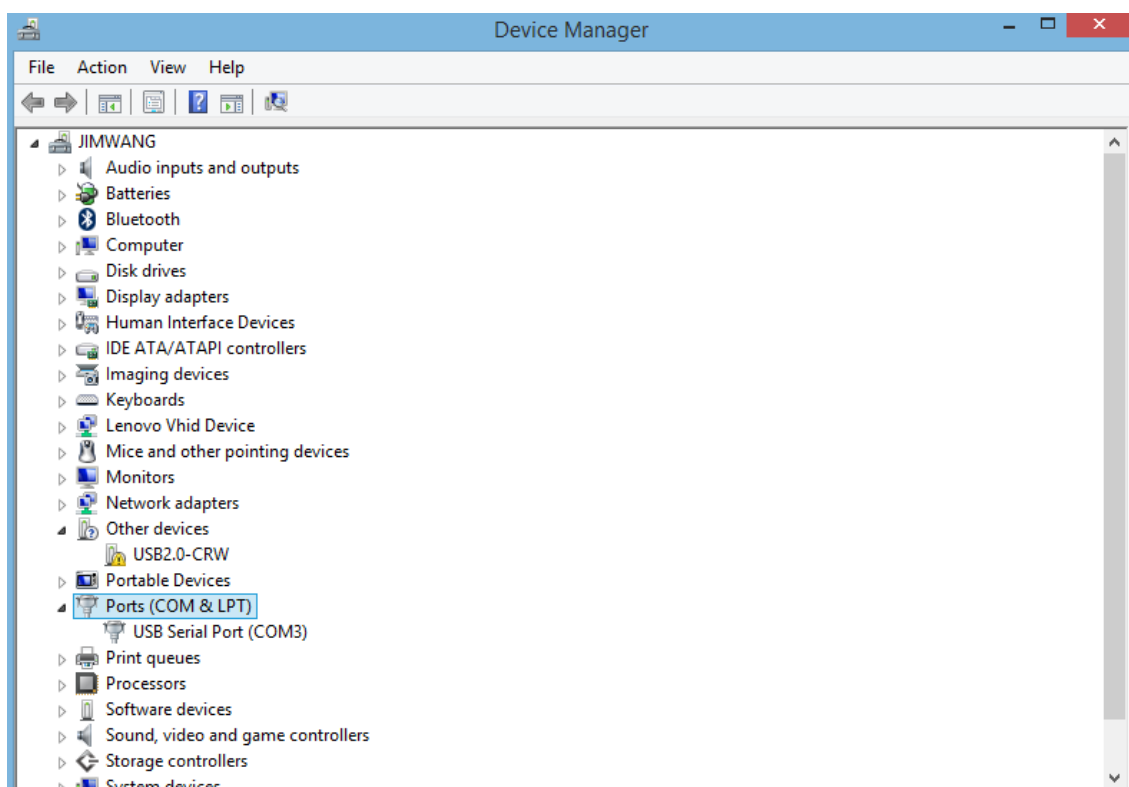
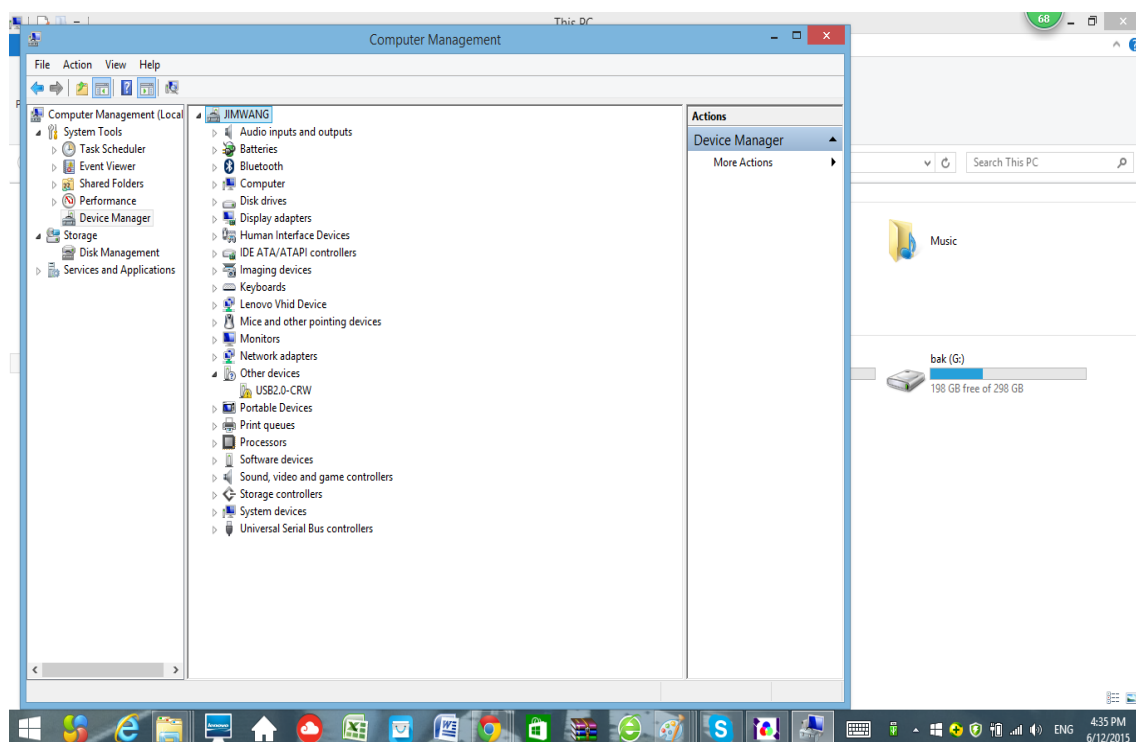
5. Click application “CA9806BERT.exe” file to run the application.

GUI Interface:

1. Find USB communication port;

After all hardware connected and CA9806 was installed in computer, Power on CA9806 BERT and Agilent 86100C mainframe. Go to “Computer Management” page to find USB port #:



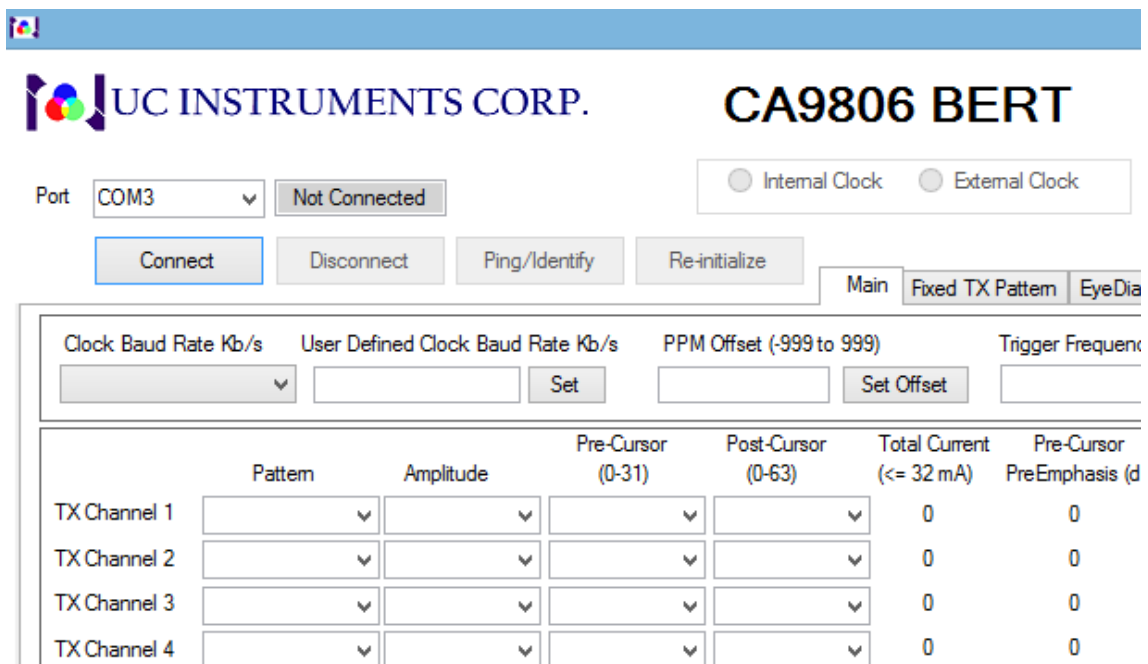


If you find above pictures' USB port location, the CA9806 software was installed and USB port was found.

If you cannot find above USB port information, you need go back to reinstall USB driver.

2. USB communication port connect

Go to CA9806 main manual, fill in USB communication port into USB port # and click connect, you will see CA9806 front panel right side LED blinking from yellow to green. That means CA9806 connected with computer GUI. CA9806 GUI port set from Not Connected changed into Connected and turn to green color.



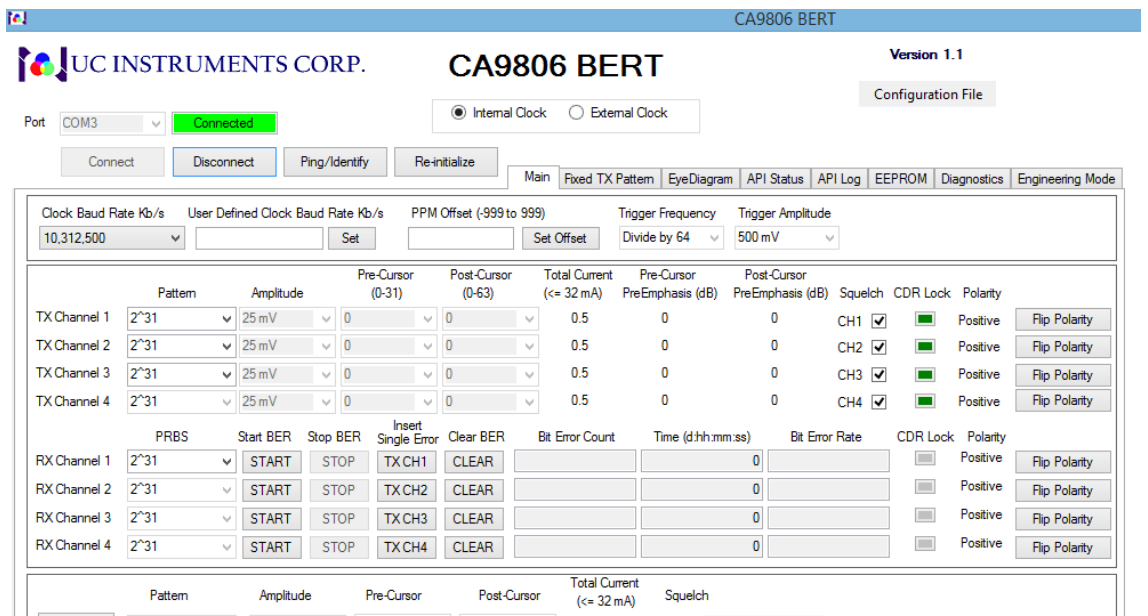
UC INSTRUMENTS CORP. CA9806 BERT

Port: COM3 Not Connected Internal Clock External Clock

Connect Disconnect Ping/Identify Re-initialize Main Fixed TX Pattern EyeDia

	Clock Baud Rate Kb/s	User Defined Clock Baud Rate Kb/s	PPM Offset (-999 to 999)	Trigger Frequency
	<input type="text"/>	<input type="text"/> Set	<input type="text"/> Set Offset	<input type="text"/>

	Pattern	Amplitude	Pre-Cursor (0-31)	Post-Cursor (0-63)	Total Current (<= 32 mA)	Pre-Cursor PreEmphasis (dB)
TX Channel 1	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	0	0
TX Channel 2	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	0	0
TX Channel 3	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	0	0
TX Channel 4	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	0	0



UC INSTRUMENTS CORP. CA9806 BERT Version 1.1

Port: COM3 Connected Internal Clock External Clock Configuration File

Connect Disconnect Ping/Identify Re-initialize Main Fixed TX Pattern EyeDiagram API Status API Log EEPROM Diagnostics Engineering Mode

	Clock Baud Rate Kb/s	User Defined Clock Baud Rate Kb/s	PPM Offset (-999 to 999)	Trigger Frequency	Trigger Amplitude
	10,312,500	<input type="text"/> Set	<input type="text"/> Set Offset	Divide by 64	500 mV

	Pattern	Amplitude	Pre-Cursor (0-31)	Post-Cursor (0-63)	Total Current (<= 32 mA)	Pre-Cursor PreEmphasis (dB)	Post-Cursor PreEmphasis (dB)	Squelch	CDR Lock	Polarity
TX Channel 1	2 ³¹	25 mV	0	0	0.5	0	0	CH1	<input checked="" type="checkbox"/>	Positive
TX Channel 2	2 ³¹	25 mV	0	0	0.5	0	0	CH2	<input checked="" type="checkbox"/>	Positive
TX Channel 3	2 ³¹	25 mV	0	0	0.5	0	0	CH3	<input checked="" type="checkbox"/>	Positive
TX Channel 4	2 ³¹	25 mV	0	0	0.5	0	0	CH4	<input checked="" type="checkbox"/>	Positive

	PRBS	Start BER	Stop BER	Insert Single Error	Clear BER	Bit Error Count	Time (d:hh:mm:ss)	Bit Error Rate	CDR Lock	Polarity
RX Channel 1	2 ³¹	START	STOP	TX CH1	CLEAR		0		<input type="checkbox"/>	Positive
RX Channel 2	2 ³¹	START	STOP	TX CH2	CLEAR		0		<input type="checkbox"/>	Positive
RX Channel 3	2 ³¹	START	STOP	TX CH3	CLEAR		0		<input type="checkbox"/>	Positive
RX Channel 4	2 ³¹	START	STOP	TX CH4	CLEAR		0		<input type="checkbox"/>	Positive

	Pattern	Amplitude	Pre-Cursor	Post-Cursor	Total Current (<= 32 mA)	Squelch
	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	0.5	<input type="text"/>

3. Main manual function.

After USB build up connection with computer GUI, we can start use CA9806 to generate PPG signal.

PPG Clock output Baud Rate set up

Go to “User Defined Clock Baud Rate Kb/s” box, fill in the clock baud rate you want and click “set” , you will see CA9806 front panel right side LED blinking from yellow to green, and the clock baud rate was set to your want. CA9806 clock baud rate can be set from 1 G to 17 G and rate.

Trigger Frequency Setup:

Click “Trigger Frequency” pull up menu, you can see “Divide by 2, 4, 8, ... 64”. We prefer default select “Divide by 64”. This will cause the smallest jitter.

Trigger Amplitude:

Click “Trigger amplitude” pull up menu, you will see the trigger out amplitude from 0 mV to 800 mW. We prefer customers select 500 mW for normal application.

TX Channel port active:

On main menu page, there are 4 Channel TX and 4 Channels RX set up check. Each CA9806 BERT with 4 channels TX ports.

Click “Squelch” check mark to select which TX channel active user want and the TX was unlocked.

PPG Pattern Mode Select :

Click TX channel “Pattern” pull up menu, customers can select PRBS 2^7-1 , 2^9-1 ,

$2^{11}-1$, $2^{15}-1$, $2^{23}-1$, $2^{31}-1$; $2^{58}-1$, Fixed Pattern that customers want.

PPG Amplitude

Click TX channel “Amplitude” pull up menu, customers can select 25 mV, 50 mV, ...500 mV, ...700 mV, ... 1600 mV, very big range. Normally customer select 700 mV output. CA9806 can provide 25 ~ 1600 mV very broad high output signal.

The screenshot displays the CA9806 BERT software interface. At the top, it shows 'UC INSTRUMENTS CORP. CA9806 BERT Version 1.1'. Below this, there are controls for Port (COM3), a 'Connected' status indicator, and clock options (Internal/External). A menu bar includes 'Main', 'Fixed TX Pattern', 'EyeDiagram', 'API Status', 'API Log', 'EEPROM', 'Diagnostics', and 'Engineering Mode'. The main configuration area is divided into several sections:

- Global Settings:** Clock Baud Rate (10,000,000), User Defined Clock Baud Rate (10000000), PPM Offset (-999 to 999), Trigger Frequency (Divide by 64), and Trigger Amplitude (500 mV).
- TX Channel Configuration:** A table with columns for Pattern, Amplitude, Pre-Cursor (0-31), Post-Cursor (0-63), Total Current (<= 32 mA), Pre-Cursor PreEmphasis (dB), Post-Cursor PreEmphasis (dB), Squelch, CDR Lock, and Polarity. Four channels are listed, all with a 2³¹ pattern and 700 mV amplitude.
- RX Channel Configuration:** A table with columns for PRBS, Start BER, Stop BER, Insert Single Error, Clear BER, Bit Error Count, Time (d.hh:mm:ss), Bit Error Rate, CDR Lock, and Polarity. Four channels are listed, all with a 2³¹ pattern.
- Summary/Action Section:** Includes a 'Clear' button, a pattern selector (2³¹), amplitude (25 mV), and cursors (0). It also has a 'Set All TX' button.
- PRBS Section:** Includes a 'Clear' button, a pattern selector (2³¹), and buttons for 'Start All BER', 'Stop All BER', 'Insert Single Error to All', 'Clear All BER', and 'Set All RX'.

TX channels also provide “Pre-cursor”, “Post-cursor”, “CDR Lock”, Polarity” select function.

Error Detector

In order to perform error detector function, normally need loop back TX signal to RX port.

After you loop back any channel TX signal to error detector(with or without DUT), click the RX channel “START” menu, Error detector start testing and “Bit Error Count”, “Time”, “Bit Error Rate” start work and show up testing data.

When you change TX clock baud rate, the CA9806 reset the setup parameters, customers need click the RX “STOP” menu, “CLEAR” menu to clear old data. Re-click “STRT” menu, the new ERROR detector testing data will be updated.

On the bottom Main menu, there are also provide “Set All TX” and “Set All RX” menu function to quick set all 4 channels TX and RX parameters quickly.

The screenshot shows the CA9806 BERT software interface. At the top, it says "CA9806 BERT" and "Version 1.1". Below this, there's a "Configuration File" button. The main menu has tabs: Main, Fixed TX Pattern, EyeDiagram, API Status, API Log, EEPROM, Diagnostics, and Engineering Mode. The "Main" tab is selected. The interface is divided into several sections:

- Top Section:** Clock Baud Rate Kb/s (10,000,000), User Defined Clock Baud Rate Kb/s (10000000), PPM Offset (-999 to 999), Trigger Frequency (Divide by 64), and Trigger Amplitude (500 mV).
- TX Channels Section:** Four channels (TX Channel 1 to 4) with settings for Pattern (2³¹), Amplitude (700 mV, 25 mV), Pre-Cursor (0-31), Post-Cursor (0-63), Total Current (<= 32 mA), Pre-Cursor PreEmphasis (dB), Post-Cursor PreEmphasis (dB), Squelch, CDR Lock, and Polarity. Each channel has a "Flip Polarity" button.
- RX Channels Section:** Four channels (RX Channel 1 to 4) with settings for PRBS, Start BER, Stop BER, Insert Single Error, Clear BER, Bit Error Count, Time (d:hh:mm:ss), Bit Error Rate, CDR Lock, and Polarity. Each channel has a "Flip Polarity" button.
- Bottom Section:** A "Set All TX" button and a "Set All RX" button.

This screenshot is similar to the previous one, but with a "Clock Frequency" dialog box open in the center. The dialog box has a title bar "Clock Frequency" and a message "Setting Frequency...". The background interface is the same as the previous screenshot, but the "User Defined Clock Baud Rate Kb/s" is now set to 14000000. The "Set All TX" button is highlighted.

CA9806 BERT

Version 1.1

Configuration File

Port: COM3 Connected

☒ Internal Clock ☐ External Clock

Connect Disconnect Ping/Identify Re-initialize

Main Fixed TX Pattern EyeDiagram API Status API Log EEPROM Diagnostics Engineering Mode

Clock Baud Rate Kb/s: 14,000,000 User Defined Clock Baud Rate Kb/s: 14000000 Set PPM Offset (-999 to 999): Set Offset Trigger Frequency: Divide by 64 Trigger Amplitude: 500 mV

	Pattern	Amplitude	Pre-Cursor (0-31)	Post-Cursor (0-63)	Total Current (<= 32 mA)	Pre-Cursor PreEmphasis (dB)	Post-Cursor PreEmphasis (dB)	Squelch	CDR Lock	Polarity	Flip Polarity
TX Channel 1	2 ⁿ 31	700 mV	0	0	14	0	0	CH1	<input type="checkbox"/>	Positive	Flip Polarity
TX Channel 2	2 ⁿ 31	700 mV	0	0	14	0	0	CH2	<input type="checkbox"/>	Positive	Flip Polarity
TX Channel 3	2 ⁿ 31	25 mV	0	0	0.5	0	0	CH3	<input checked="" type="checkbox"/>	Positive	Flip Polarity
TX Channel 4	2 ⁿ 31	25 mV	0	0	0.5	0	0	CH4	<input checked="" type="checkbox"/>	Positive	Flip Polarity

	PRBS	Start BER	Stop BER	Insert Single Error	Clear BER	Bit Error Count	Time (d:hh:mm:ss)	Bit Error Rate	CDR Lock	Polarity	Flip Polarity
RX Channel 1	2 ⁿ 31	START	STOP	TX CH1	CLEAR		0		<input type="checkbox"/>	Positive	Flip Polarity
RX Channel 2	2 ⁿ 31	START	STOP	TX CH2	CLEAR	3613657997	0.00:02:16	0.0018979	<input checked="" type="checkbox"/>	Positive	Flip Polarity
RX Channel 3	2 ⁿ 31	START	STOP	TX CH3	CLEAR		0		<input type="checkbox"/>	Positive	Flip Polarity
RX Channel 4	2 ⁿ 31	START	STOP	TX CH4	CLEAR		0		<input type="checkbox"/>	Positive	Flip Polarity

Pattern Amplitude Pre-Cursor Post-Cursor Total Current (<= 32 mA) Squelch

CA9806 BERT

Version 1.1

Configuration File

Port: COM3 Connected

☒ Internal Clock ☐ External Clock

Connect Disconnect Ping/Identify Re-initialize

Main Fixed TX Pattern EyeDiagram API Status API Log EEPROM Diagnostics Engineering Mode

Clock Baud Rate Kb/s: 14,000,000 User Defined Clock Baud Rate Kb/s: 14000000 Set PPM Offset (-999 to 999): Set Offset Trigger Frequency: Divide by 64 Trigger Amplitude: 500 mV

	Pattern	Amplitude	Pre-Cursor (0-31)	Post-Cursor (0-63)	Total Current (<= 32 mA)	Pre-Cursor PreEmphasis (dB)	Post-Cursor PreEmphasis (dB)	Squelch	CDR Lock	Polarity	Flip Polarity
TX Channel 1	2 ⁿ 31	700 mV	0	0	14	0	0	CH1	<input type="checkbox"/>	Positive	Flip Polarity
TX Channel 2	2 ⁿ 31	700 mV	0	0	14	0	0	CH2	<input type="checkbox"/>	Positive	Flip Polarity
TX Channel 3	2 ⁿ 31	25 mV	0	0	0.5	0	0	CH3	<input checked="" type="checkbox"/>	Positive	Flip Polarity
TX Channel 4	2 ⁿ 31	25 mV	0	0	0.5	0	0	CH4	<input checked="" type="checkbox"/>	Positive	Flip Polarity

	PRBS	Start BER	Stop BER	Insert Single Error	Clear BER	Bit Error Count	Time (d:hh:mm:ss)	Bit Error Rate	CDR Lock	Polarity	Flip Polarity
RX Channel 1	2 ⁿ 31	START	STOP	TX CH1	CLEAR		0		<input type="checkbox"/>	Positive	Flip Polarity
RX Channel 2	2 ⁿ 31	START	STOP	TX CH2	CLEAR	0	0.00:03:10	0.0E-13	<input checked="" type="checkbox"/>	Positive	Flip Polarity
RX Channel 3	2 ⁿ 31	START	STOP	TX CH3	CLEAR		0		<input type="checkbox"/>	Positive	Flip Polarity
RX Channel 4	2 ⁿ 31	START	STOP	TX CH4	CLEAR		0		<input type="checkbox"/>	Positive	Flip Polarity

Pattern Amplitude Pre-Cursor Post-Cursor Total Current (<= 32 mA) Squelch

4. Build-in 8.5 ~ 15 Gbps Eye Diagram Function

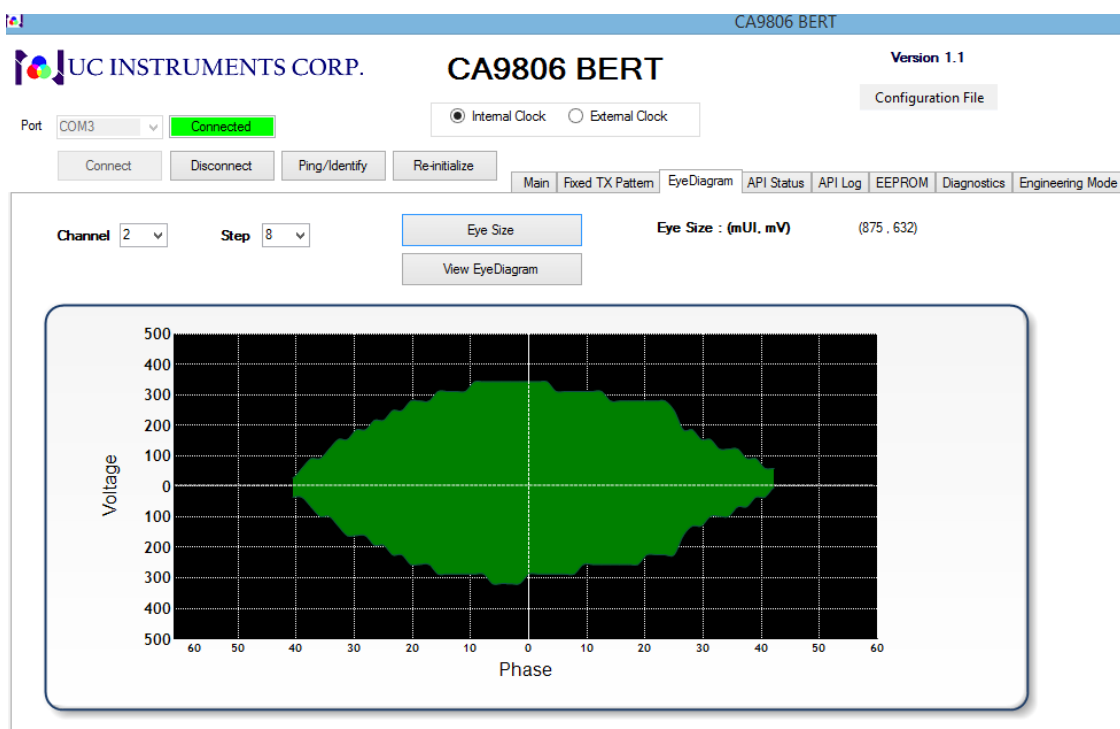
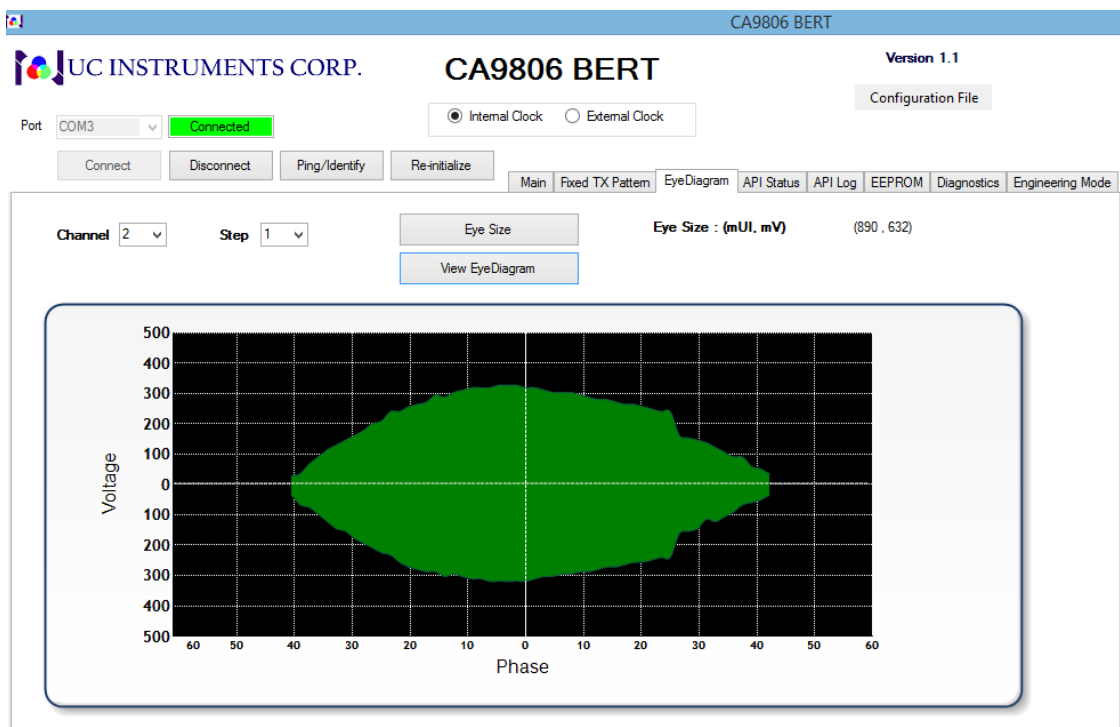
CA9806 provide a special build-in 8.5 ~ 15 Gbps eye diagram function. Customers can use it to verify 8.5 ~ 15 Gbps DUT eye diagram performance.

Loop back TX channels with RX channels (or connect after DUT unit RX), Switch "Main" menu to "EyeDiagram" menu. Select the "Channel" (Channel 1 ~ 4 selectable). Select "Step" (there are Step options 1, 2, 4, 8. The step 1 is the

lowest speed and the highest resolution testing. Step 8 is the most fast and the lowest resolution testing).

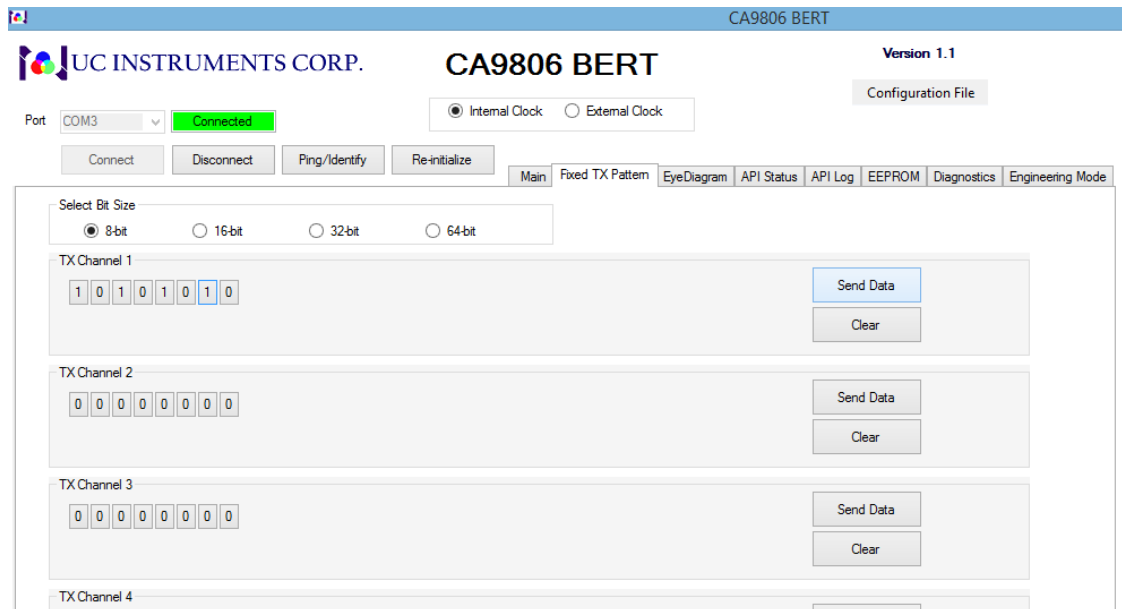
Click “Eys Size” menu, to get testing range;

Click “View EyeDiagram”, start to perform eye-diagram testing.

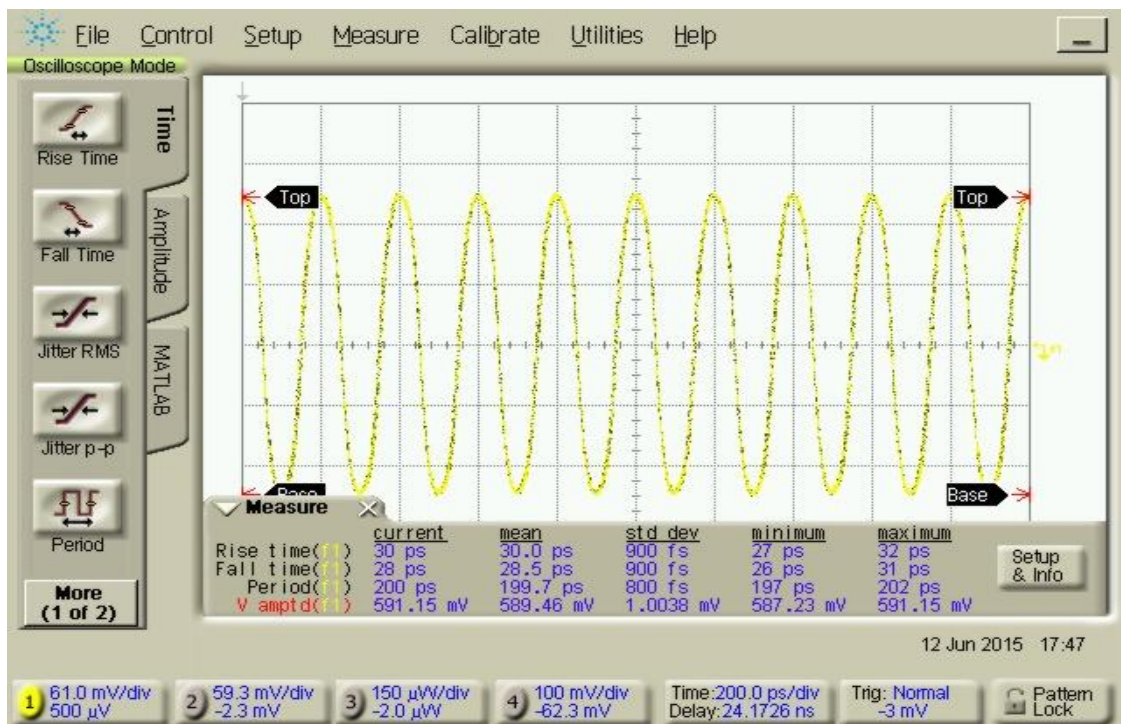


5. Fixed TX Pattern Function

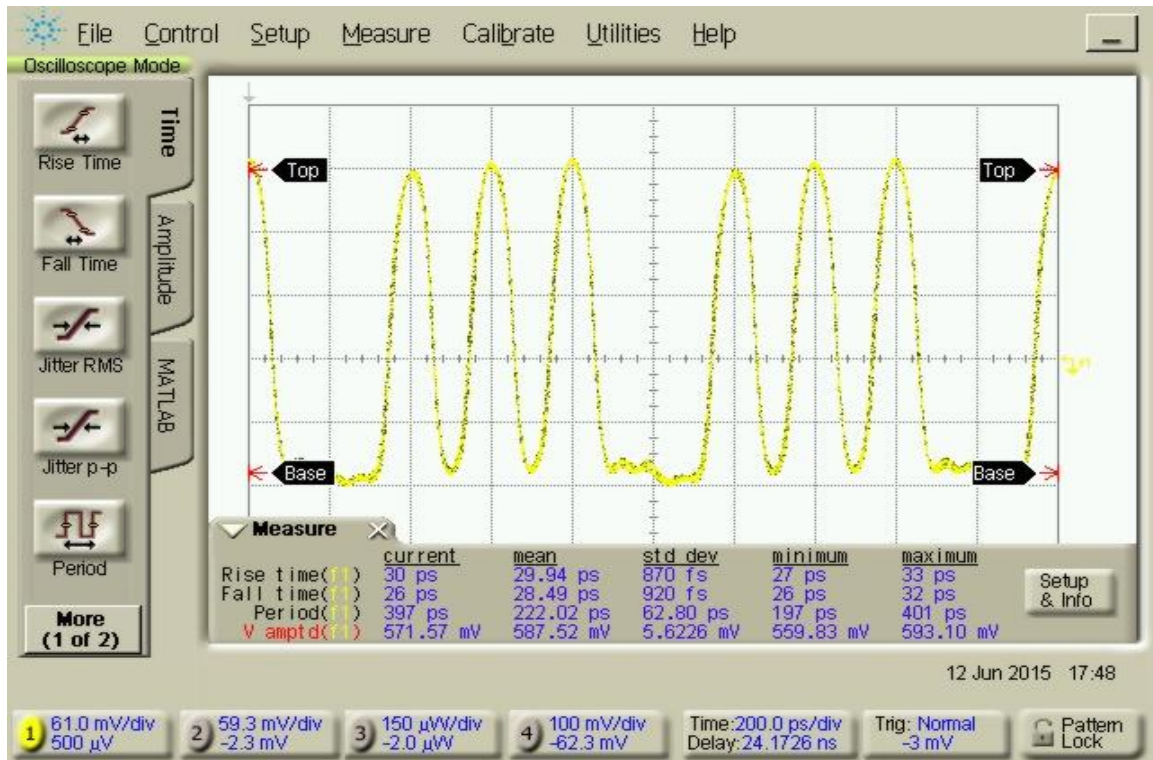
On “Main” menu, we select TX Channel 1 Pattern mode as “Fixed Pattern”. Switch the “Main” menu to “Fixed TX Pattern” interface. Set “Select Bit Size” as 8 bit.



Set TX Channel 1 bit size as “10101010”. Click “Send Data”, we can get fixed TX pattern from Agilent 86100A + 83483A module screen as below:



Set TX Channel 1 bit size as "10100010". Click "Send Data", we can get fixed TX pattern from Agilent 86100A + 83483A module screen as below:



CA9806 Fixed TX Pattern function makes CA9806 as a 4 channels output
0.5 ~ 8.5 GHz signal generator.

6. Others Function

Status Control

- Status control tab
- Read status of all control settings using the API command set
- Click on the description link to find out the API command set for each line item.

EBERT 1504

ESOR ENGINEERING **EBERT 1504** Version 1.0.10.2

Port: COM16 Connected Internal Clock External Clock Configuration File

Connect Disconnect Ping/Identify Main Fixed TX Pattern EyeDiagram Status API Log EEPROM Diagnostics Engineering Mode

Read Status

	Clock	Channel 1	Channel 2	Channel 3	Channel 4	Description
Voltage TX Level	10000000	00250000	00250000	00250000	00250000	View
Squelch TX	N/A	1	1	1	1	View
Pattern TX	N/A	02	02	02	02	View
CDR Pattern RX	N/A	2	2	2	2	View
Fixed Pattern	FFFFFFFF00000000	0000000000000000	0000000000000000	0000000000000000	0000000000000000	View
Polarity	N/A	11	11	11	11	View
Clock Source	0					View
Nominal Clock PPM Offset	0000					View
Baudrate	10312500					View

API Control

- API control tab
- View all API commands that have been sent and received to the 1504 unit, used for getting familiar with the API structures

EBERT 1504

ESOR ENGINEERING **EBERT 1504** Version 1.0.10.2

Port: COM16 Connected Internal Clock External Clock Configuration File

Connect Disconnect Ping/Identify Main Fixed TX Pattern EyeDiagram Status API Log EEPROM Diagnostics Engineering Mode

Send Command

G4C
G10
G20
G30
G40
G0F
G1F
G2F
G3F
G4F
G1K
G2K
G3K
G4K
G1N
G2N
G3N
G4N
G1T
G2T
G3T
G4T

Receive Command

2
11
11
11
11
FFFFFFFF00000000
0000000000000000
0000000000000000
0000000000000000
0000000000000000
0000000000000000
0
0
0
0
0000
0000
0000
0000
10312500
10312500
10312500
10312500

EEPROM control tab

- Read firmware version button
- Read Factory EEPROM contents and fan hour meter

EBERT 1504

ESOR ENGINEERING EBERT 1504 Version 1.0.10.2

Port COM16 Connected Internal Clock External Clock Configuration File

Connect Disconnect Ping/Identify Main Fixed TX Pattern EyeDiagram Status API Log EEPROM Diagnostics Engineering Mode

Read Firmware Version EBERT 1504 Firmware Version #2.1 - 150210

Read EEPROM

Serial Number 0004

Manf Build Date 0915

Manf Build Year 2014

Firmware Version 0021

PCB Version 0001

Rework Version 0001

Serdes IC REV 00A1

Serdes EEPROM Date 0915

Serdes EEPROM Year 2014

Fan Hour Meter 00000021

Diagnostics Control Tab

- Read status button
- Power supply voltage rails of the system and fan rotation

EBERT 1504

ESOR ENGINEERING EBERT 1504 Version 1.0.10.2

Port COM16 Connected Internal Clock External Clock Configuration File

Connect Disconnect Ping/Identify Main Fixed TX Pattern EyeDiagram Status API Log EEPROM Diagnostics Engineering Mode

Read Status

IC Voltage

Description	Channel 1	Channel 2	Channel 3	Channel 4
Temperature (Celsius)	38	38	37	37
0.9V Supply (mV)	890	890	898	897
1.8V Supply (mV)	1795	1794	1805	1805

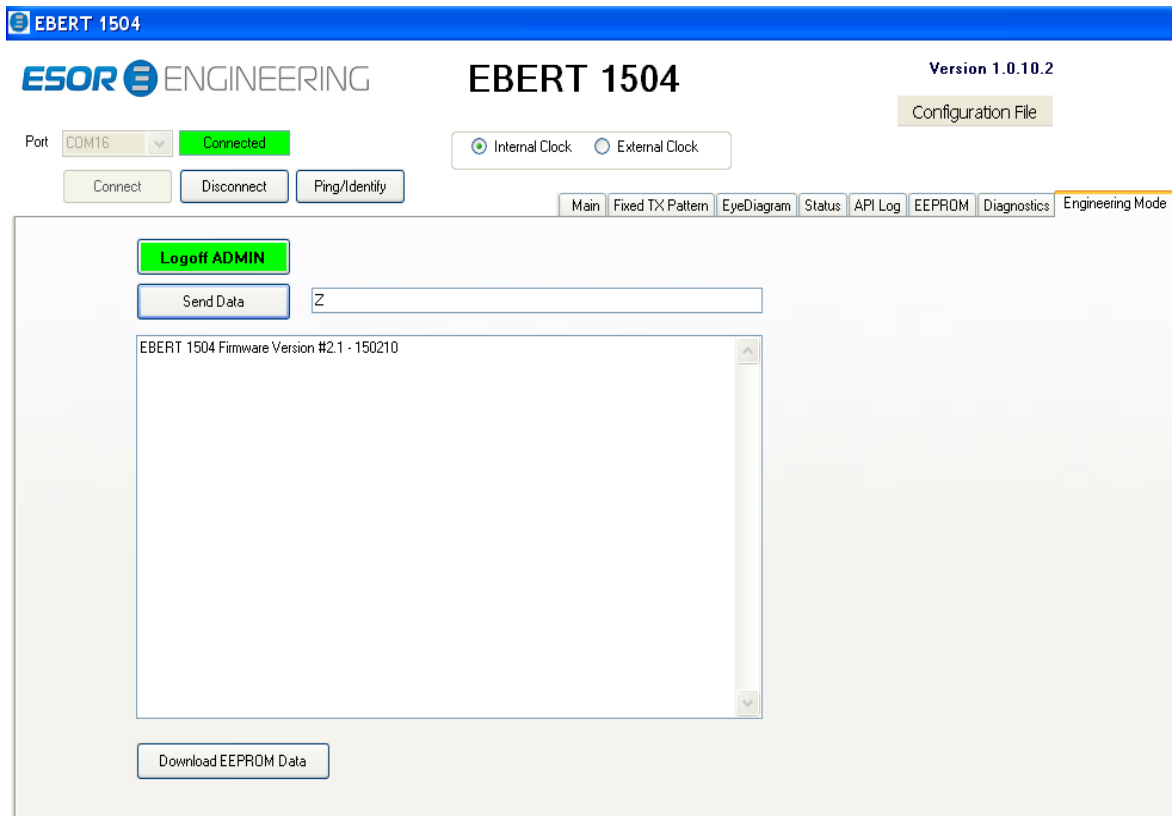
Power Supply Voltage

0.9V (mV)	919
1.8V (mV)	1844
2.5V (mV)	2521
3.3V REFCLK (mV)	3357
3.3V REFCLK (mV)	3362
3.3VMICRO (mV)	3357
5.0V (mV)	4926
Fan (mV)	1810

** Fan tachometer average output should read between 1V and 2.5V to ensure its rotating correctly

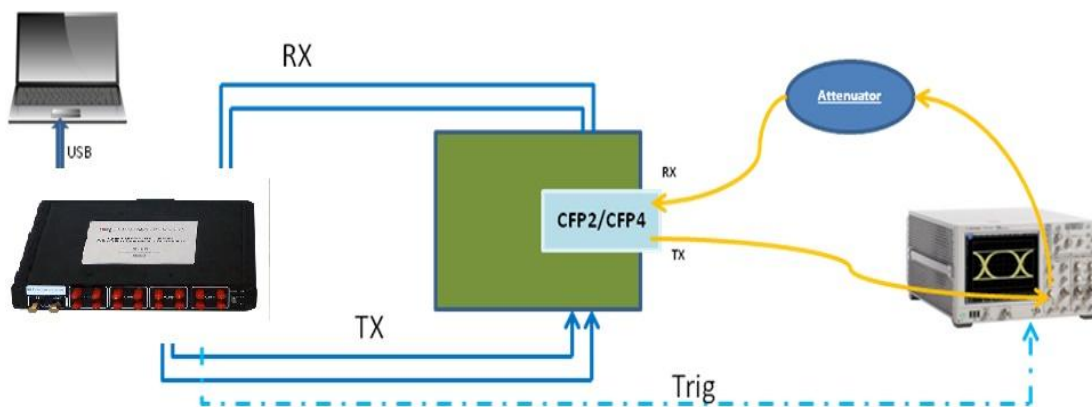
Engineering control tab

- Send API commands, warning as you are in master mode and the GUI won't be in synchronization with the commands being sent
- Download EEPROM contents, only used at the factory

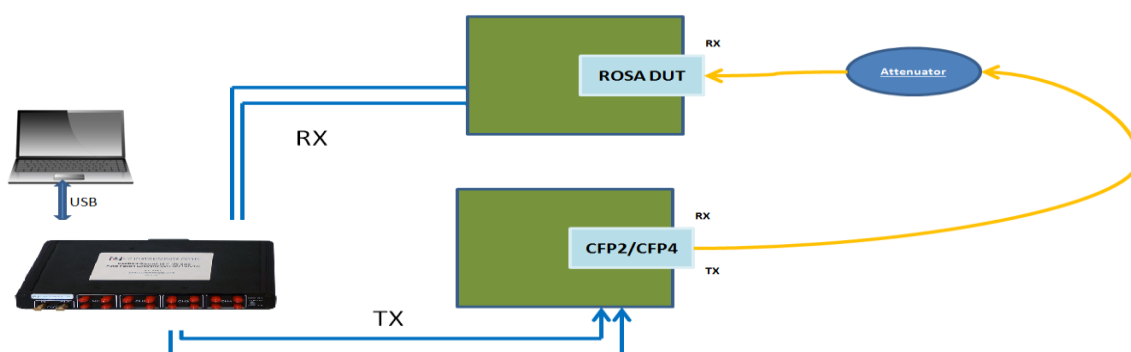


Typical Test Application and Connecting

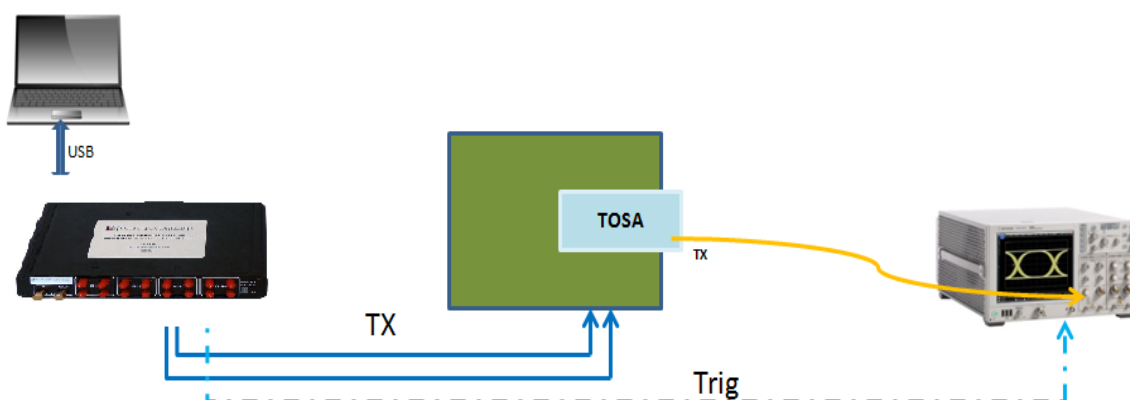
CFP2/CFP4/QSFP14 module Test Diagram



CFP2/CFP4/QSFP14 ROSA Test Diagram



CFP2/CFP4/QSFP14 TOSA Test Diagram



Flexible Reconfiguration

Three sets CA9806 can be integrated into to a 12 X 15 Gbps(180Gbps)
Testing system

Claims and Repackaging

If physical damage is evident or if the instrument does not meet specification when received, notify the carrier and the UC Instruments Corp. Maintenance Service Center. The Maintenance Service Center will arrange for repair or replacement of the unit without waiting for settlement of the claim against the carrier.

Return Shipments to UC Instruments Corporation

If the instrument is to be shipped to a UC Instruments Corp. Maintenance Service Center, attach a tag showing owner, return address, model number and full serial number and the type of service required.

The original shipping carton and packing material may be reusable, but the UC Instruments Corp. Maintenance Service Center will provide information and recommendation on materials to be used if the original packing is no longer available or reusable.

General instructions for repackaging are as follows:

- Wrap instrument in heavy paper or plastic.
- Use strong shipping container.
- Use enough shock absorbing material around all sides of the instrument to provide a firm cushion and prevent movement inside container. Protect control panel with cardboard.
- Seal shipping container securely.
- Mark shipping container **FRAGILE** to encourage careful handling.

- In any correspondence, refer to instrument by model number and serial number.

Maintenance

- Avoid sharp vibration when operation.
- Keep the head face of sensor clean.
- Cover the channel adaptor on the front panel with the dust cap.
- Don't forcibly push or drag the connector out of the adaptor of CA9806.
- Be careful for crash and fall-off.

UC INSTRUMENTS CORP. CONTACT INFORMATION

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